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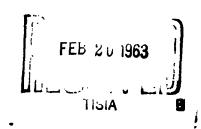
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U.S. ARMY ELECTRONICS MATERIEL AGENCY
CONTRACT NO. DA-36-039-SC-86726
QUARTERLY REPORT FOR THE PERIOD
JULY 31, 1962 TO OCTOBER 30, 1962



#### PRODUCTION ENGINEERING MEASURE

2N914 AND 2N995

QUARTERLY REPORT
FOR THE PERIOD

JULY 31, 1962 TO OCTOBER 30, 1962

#### **OBJECT:**

IMPROVE PRODUCTION TECHNIQUES TO INCREASE THE RELIABILITY OF SILICON PLANAR EPITAXIAL TRAN-SISTORS 2N914 AND 2N995

CONTRACT NO. DA-36-039-SC-86726 ORDER NO. 19048-PP-62-81-81

PLACED BY
U.S. ARMY ELECTRONICS MATERIEL AGENCY
PHILADELPHIA, PENNSYLVANIA

PREPARED BY: D. M. KOBRIN APPROVED BY: P. W. HILL

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#### SECTION I PURPOSE

Contract No. DA-36-039-SC-86726 is a Production Engineering Measure for improvement of production techniques to increase the reliability of 2N914 and 2N995 transistors. Processes to be improved include.

- a. Lead Attach
- b. Preparation of Substrate for Epitaxial Growth
- c. Header Plating
- d. Particle Elimination
- e. Die Attach

## SECTION II ABSTRACT

This second quarterly report presents a detailed narrative of the portion of the work accomplished in August, September, and October of 1962, in each task under PEM contract No. DA-36-039-SC-86726. Data, sketches, and reports relative to the second quarter's work are appended to the discussion of each task.

#### NARRATIVE AND DATA, CONCLUSIONS, FUTURE PROGRAM

Each of the nine subsections which follow has been authored by the engineer in charge of the task described. Because the projects are relatively autonomous the narrative, conclusions, data, and plans for each project have been consolidated in these subsections, rather than presented in separate sections of the report.

A cover sheet precedes each subsection, identifying the material contained within it.

#### TASK 1A - CHANGE IN METAL SYSTEM

#### CONTENTS

#### Purpose

#### Background

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Exhibit 1 - Processes Used For Gold Over Palladium Metallization

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Exhibit 5 - Table Showing Longest Alloying Times to Allow Pure Palladium

Pattern Surfaces and Shortest Alloying Times to Produce Palladium

and Silicon Pattern Surfaces

#### TASK 1A - CHANGE IN METAL SYSTEM

W. J. Warren

#### **PURPOSE**

The capillary bonds between gold wire and aluminum patterns on small geometry devices sometimes fail at the gold-aluminum interface during life testing at temperatures greater than 200°C. This is due to diffusion of gold into aluminum at elevated temperatures, forming a third composition AuAl<sub>2</sub>. The large scale atomic diffusion causes voids to form at the bonded interface, and this causes mechanical bond failure.

The purpose of this project is to determine the feasibility of developing alternate systems to the gold-wire-aluminum-pattern system presently used.

It is desired that any new systems should maintain the ball bonding thermal compression technique if possible.

It was initially decided that metallized layers of platinum and palladium should be investigated. It was decided that silver should be investigated also since it had desirable properties such as nobility, the formation of no intermetallics with gold, and low resistivity. Also, it was reported by the Research and Development department at Fairchild that silver could be easily evaporated and that the silver pattern did not affect the electrical parameters of transistors.

#### BACKGROUND

It was proved in the last period that palladium metallized devices could be made with satisfactory electrical parameters. However, problems were found to exist in lead bonding gold wire to the metallized dice.

#### PROGRESS DURING THIS PERIOD

As stated in the last report, ninety-two palladium metallized dice were lead bonded after die attach. However, the yield for the lead bonding operation was very low and it was therefore resolved to attempt to lead bond

before die attach. This allowed temperatures higher than the AuSi eutectic temperature (370°C) to be used to form the Au-Pd capillary bonds.

Dice fabricated by the process described in the last report were held in the hard chromium-plated brass jig described in that report. Lead bonding to the dice was attempted under different load, time and temperature conditions. At 400°C, weak bonds were obtained between the palladium and the gold. However lead bonding before die attach was found to present two serious problems:

- It was extremely difficult to remove the lead-bonded dice from the holding jig and then perform the die attach operation without mechanically dislodging the bonded leads;
- 2) The metal mask often did not exactly cover the emitter and base regions, leaving some silicon surface bordering the palladium. When the gold ball overlapped the palladium and touched the silicon then the ball dissolved as molten gold-silicon eutectic at temperatures greater than 370°C.

These disadvantages caused the abandonment of further attempts at lead bonding before die attach. It was resolved that further tests should involve conventional low temperature (345°C ± 10°) lead bonding after die attach.

Several different approaches were tried using different metallized layers and different wires: (a) evaporating a layer of gold over the palladium to test the properties of a gold wire to gold surface capillary bond; (b) using a silver metallized layer with either gold, palladium, or silver wire; (c) using palladium as a metallized layer with different metallizing processes than those previously tried.

Further details of these approaches are given below:

### 1. Metallized layer: Gold over palladium Wire: Gold

Different amounts of palladium and gold and different alloying schedules were tried as shown in exhibit 1 at the end of this report. Bo nding tests to the specimens showed the following results: (a) At bonding temperatures greater

else causing such bad undercutting at the edges of the emitter and base rings that the adhesion between the silver and silicon was seriously weakened. One method of KPR removal which was used was to burn off the KPR at high temperature, allowing further alloying between the silver and the silicon to occur simultaneously. Another satisfactory method was to dissolve away the KPR by soaking the wafers in acetone.

- (f) Bonding tests were carried out on dice after the die attach operation where this was possible. However, occasionally preliminary bonding tests were carried out at an earlier step, for instance to the wafer after alloying and before etching. This was done if it was thought possible that the etching operation might ruin the metal pattern. In some cases only approximate estimates were made for the frequency of bonding. However, records were kept for small bonding samples showing the relative numbers of successful and unsuccessful bonds. It was also noted whether lack of success was due to lack of sticking of the gold ball to the silver caused by unfavorable silver surface condition, or due to the silver layer being torn away from the silicon owing to poor silver silicon alloying.
  - (g) Several methods of mechanically evaluating the bonds were tried:
- 1. Standard in-plant centrifuge tests at 40,000 g's for two minutes were carried out on canned devices. The cans were removed after the test and visual examination showed whether the bonds had failed or not, and where failures had occurred (i.e. under the ball or under the metal).
- 2. The wires were pulled with tweezers until bond failure occurred. This test was used on bonds to wafers and to dice mounted on headers. It was observed whether failure occurred in the span of the wire, or under the ball, or under the metal pattern. Ideally, all failures would occur in the span of the wire; this happened when pulling forth bonded aluminized devices used as a standard reference.

- 3. The bonded wires were attached to a dynamometer and pulled until failure. As well as observing where bond failure occurred, the dynamometer reading, showing the bond breaking strength in grams, was recorded. The pull strengths could be compared with those obtained on pulling .002" diameter gold wire bonded to aluminized 2N696 units:
  - 20 bonded emitters...wires broke in span...mean pull strength
    20.3 grms.
  - 20 bonded bases ...wires broke in span...mean pull strength
    21.4 grms.
- (h) One simple test was used to evaluate the effect of heat on the bonds. Bonded units were aged in an oven at 300°C in a nitrogen atmosphers. They were removed from the oven and inspected for bond failure from time to time.

  COMMENTS ON THE TESTS PERFORMED.

Runs 1, 2 and 3 (in exhibit 2 at the end of this report) were all ruined at the KPR removal stage because the chromic acid attacked the silver metal. However, some bonds were made to wafers of runs 2 and 3 before etching. The results of pulling bonds to run 3 oxidized wafers showed that the aluminum under the silver adhered well to silicon dioxide after the 580°C, 3-minute alloying treatment.

Some lifting of the silver pattern occurred in run 4. The reason for this was that the wafers were etched before the alloying treatment coincidental with KPR removal; hence the etchant seeped in at the silver-silicon interface and attacked the silver. The lifting of the metal caused many of the pulled bonds to fail beneath the metal.

Run 5 was unetched in order to test the bonding properties of unetched silver. The pull tests showed that the bonds made were of considerable strength, although the centrifuge test caused some failures below the silver pattern. This suggested that the total alloying of 830°C for 6 minutes was insufficient to bond the silver to the silicon. The results of high tempera-

ture aging showed that bond failure began to occur after 40 hours at 300°C in nitrogen. On inspecting the failures due to aging it appeared that the silver had diffused into the gold ball leaving a void beneath the gold ball.

Runs 6 and 7 were not successful owing to the high 830°C alloying temperature used. The aluminum-silicon eutectic melted at 580°C and hence most of the aluminum fused and flowed at 830°C.

A long alloying time was used for run 8 since 830°C for 6 minutes was shown to be insufficient in run 5. Bonding of gold wire to the silver was usually successful, although some metal lifting showed that alloying was not wholly successful, and occasional non-adherence of the gold to the silver was perhaps due to oxidation of the silver surface by the ferric nitrate.

Aging some bonds made with gold wire supported previous evidence that failure began after 40 hours at 300°C.

Palladium wire did not capillary bond to run 8 units.

Some success was obtained in capillary bonding silver wire to run 8 units. Conclusions.

The failure of capillary-bonded gold wire during the 300°C life test after 40 hours was a serious disadvantage to the use of silver metallization. The substitution of silver wire for gold wire showed some promise, even though the silver wire fused to form tear-drop balls which were hard to work with instead of the easily bondable round balls that gold formed. Palladium wire did not thermo-compression bond to silver patterns.

A satisfactory alloying schedule has not yet been found in the present tests, but longer times than those tried should be effective.

## 3. Metallized layer: Paladium wire: Gold

Processes used for metallizing, bonding, and evaluating bond strength are described in exhibit 3 at the end of this report.

Metallizing and alloy methods were the same for palladium as for silver.

However, agua regia at room temperature was used as an etchant for the

palladium. Owing to the poor adherence of Pd to silicon dioxide, the use of Kodak Photo Resist or Kodak Metal Etch Resist masking prior to etching was sometimes unnecessary; a very short aqua regia etch was often sufficient to float the unwanted palladium off the oxide without seriously affecting the metal over the silicon. However, metal masking was necessary for small geometry 2N914 and 2N995 devices, since it was otherwise found to be very hard to remove the metal over the oxide between emitter and base. After etching masked small geometry units, the KPR or KMER was removed by 80°C chromic acid or by acetone.

Methods of bonding and evaluating bond strength were the same as those previously described for silver.

As can be seen from exhibit 3, many different metal evaporations and alloy schedules were tried, in order to obtain a process which would give a metal layer with a clean, pure palladium surface. Bonding tests to a thin sheet of pure palladium showed that gold wire would capillary bond to it perfectly at normal bonding temperatures (345°C); when the bonds were pulled, failure occurred in the span of the wire every time showing that gold-palladium bonds were of a strength comparable to gold-aluminum bonds.

Many complex materials, of differing colors and textures, appeared in evaporated palladium in the course of the processes attempted. It was difficult to deduce in every case the exact nature of the palladium compound produced; however, these compounds usually hindered either the palladiumsilicon alloying or palladium-gold bonding to some degree.

A discussion of some of the compounds seen:

A pink oxide appeared on palladium at 300°C or higher in air. The oxide appeared when wafers or dice were left on the heater block for more than a minute during the lead bonding operation. However, the oxide did not seem to impair the bonding properties between the gold ball and the palladium.

A thick black deposit appeared over the emitter dot and base rings of runs #7, 8, 9, 10. The deposit was first observed after the evaporation and before the alloying step, and this was only visible in palladium over silicon and not in palladium over oxide. Scratching the black deposit revealed that it extended throughout the metallized layer down to the silicon substrate. It was formed in the four runs in which the wafers were heated to about 600°C on tantalum strips during the evaporation. Since the evaporation took place in an evacuated bell jar, it does not seem likely that the deposit was an oxide. It is thought that the deposit was formed by rapid interdiffusion of silicon and palladium during the evaporation, and is an intimate mixture of silicon and palladium. This would explain why it occurred throughout the metallized layer over silicon, did not occur over silicon dioxide, and did not depend on the presence of oxygen. The black meterial was not encountered again after heating of the wafers during evaporation was discontinued.

A brown material was observed over the silicon only, not over the silicon dioxide, in runs #17, 25, 28. It appeared after the alloying treatment and existed as a thin layer over the white palladium beneath. The layer structure over palladium suggests strongly that the brown deposit was an oxide. This was supported by the fact that it was only formed during long alloying treatments of 20 minutes or more, since an appreciable amount of oxygen might be carried into the alloying furnace with the inert gas ambient over these prolonged times. However, since the oxide was only formed over palladium over the silicon and not palladium over silicon dioxide, it was probably a complex oxide of silicon and palladium.

Many of the problems encountered using palladium metallization were due to palladium-silicon interdiffusion and to oxide formation. Two approaches were tried to overcome these disadvantages - 1) double evaporation and 2) short alloying times.

The principle of double evaporation was that a layer of pure palladium be evaporated over the alloyed, and possibly impure, first palladium layer. Bonding to the pure second layer of palladium would then be easy. Problems were encountered in obtaining satisfactory bonding between the first and the second palladium layers. For instance, in run #27, bond failure always occurred at the interface between the first and second metal layers. Therefore, in run #29, a second alloying step was added after the second evaporation. Thirty 2N696 units were lead bonded and lead welded without loss, even though some bonds pulled to destruction still failed between the first and second metal layers. Run #30 used the same metallizing and alloying steps as run #29, but on small geometry 2N995 PNP units. Lead bonding problems were encountered in run #30 since it was not found possible to remove all of the KMER from the emitter dots and base rings after etching off the unwanted metal. About twenty lead bonded and welded units were nevertheless produced. Double evaporations with double alloying were tried in runs #31, 32, 33 on small geometry, NPN, devices. It was found impossible to remove the KMER from run #32, or the PKR from run #33, after etching; this led to poor lead bonding results. Lifting of the metal between the first and second metal layers occurred when bonds to run #31 wafers were pulled to failure.

The KMER or KPR removal problem was one that often occurred during the small geometry runs. KMER was first used as a masking material, and KPR was later tried in the hope that it would give better results; however, the same problem was encountered with KPR as with KMER. Long etching times of approximately 5-10 minutes in 80°C chromic acid were needed to remove the masking material, and it was found that the chromic acid attacked and removed the palladium simultaneously. It was therefore decided to use acetone for removal of the maskant. The masked wafers were alternately ultrasomically cleaned in an acetone bath and lightly swabbed with acetone-soaked cotton buds, until the KPR or KMER was removed. This method was successfully used for run #39.

Since it was found to be difficult to get perfect adhesion between the first and second metal layers using a double evaporation, another approach to the metallization problem was tried; this was the second approach referred to previously. Short alloy times were used since this allowed less time for oxidation or for palladium-silicon interdiffusion to occur at the alloying temperatures. In runs #34-41, greater amounts of palladium were evaporated since it was believed that this would make a longer path for passage of silicon to the palladium surface; therefore contamination of the palladium surface by silicon would be less likely.

Runs #34, 35, 36 were of considerable interest in showing evidence of diffusion of palladium into silicon. In these runs, 3000 mg of palladium were evaporated from the tungsten filament. In run #34 wafers were alloyed for half a minute at 725°C. A clean, thick palladium surface was produced (see exhibit 4) to which forty bonds, all breaking in wire span on pulling, were made. In run #35 alloying was for three-quarters of a minute at 725°C and some greyish palladium-silicon appeared in some emitter and base areas. Run #36 was alloyed for one minute and more palladium-silicon areas were visible in emitters and bases (see exhibit 4). One interesting fact was that the run #6 photo showed a downward step from the palladium to the dark palladium silicon area. This very strongly suggested that the rapid diffusion of palladium into silicon was a dominant mechanism of palladium-silicon mixing.

A table was drawn up showing those alloying treatments used which were the longest to allow a pure palladium surface, and the shortest to show a palladium-silicon mixture. This table may be found in exhibit 5 at the end of this report. The table shows the critical alloying treatments for different amounts of palladium evaporated. Since there were three variables - alloy time, alloy temperature, and mg palladium evaporated - and only a limited amount of information, it was not considered practicable to present the information in graphical form. However, definite trends may be seen from the results.

Longer alloying times at higher temperatures were needed to produce the palladium-silicon mixtures for wafers with a heavier deposition of palladium.

This is the result which would be expected for interdiffusion of palladium and silicon, and justified the decision to evaporate thick layers of palladium.

It was found better to try alloying at as low a temperature as possible, and for a long time, rather than alloying for very short times at higher temperatures. The latter approach is not preferable since it was found inconvenient to anneal for times within very critical limits. Annealing schedules of 300°C for 2 hours in nitrogen were used on runs #39 and #41, and this was found to be very promising. Bonds were easily made to wafers after alloying, and nearly all pulled bonds broke in the span of the wire. However, trouble was encountered in lead bonding satisfactorily after the die attach step in run #39. Bonds could be made reproducibly, but about 50% of them were not strong enough to survive the lead weld step. Even so, fifty completed devices from this run have been produced so far with good electrical characteristics. The deterioration in bonding properties after masking and etching was probably due to some KPR being left on the emitter and base areas.

Tests were carried out to find the effects of high temperature aging on bonded palladium metallized units. Fourteen bonded units of run #7 were aged at 300°C in N<sub>2</sub> for 300 hours. Visual examination showed that no failures had occurred after this time.

#### Conclusions:

Palladium metallization can be used to give units with good electrical properties, which will survive aging at  $300\,^{\circ}\text{C}$  in  $N_2$  for 300 hours without bond failure..

The thickness of the palladium layer and the alloying schedule are critical since interdiffusion of palladium and silicon, which in an advanced stage will ruin bonding properties, occurs rapidly at high temperatures.

The removal of maskant, after etching away unwanted metal is still a problem.

Silver and palladium both appear to be promising as replacements for aluminum.

However, silver patterns cannot be used with gold wire since bond failure occurs after forty hours in N at 300°C. A replacement must be found for the gold lead wire.

Palladium can be used to metallize silicon transistors without any apparent degradation of the transistor parameters. However, the metallizing and alloying process is quite critical. Since gross palladium-silicon interdiffusion will deteriorate the lead bonding properties of the palladium surface.

PROGRAM FOR NEXT INTERVAL

Further work is planned to find a metallizing and alloying process for palladium which will give reproducible good results. Life and environmental tests will be performed on 250 devices produced by this process.

Attempts will be made to find a replacement for gold wire to be used with silver patterns.

TASK 1A - EXHIBIT 1

PROCESSES USED FOR GOLD OVER PALLADIUM METALLIZATION

	76		lst Evaporation	ion		2nd Evaporation	ion	1
Run No.	Alloy 630°C 4-1/2M	mg Au	Alloy Time (m)	Alloy Temp °C	mg Au	Alloy Time (m)	Alloy Temp °C	
1	700	200	7/1-7	007	300	None		
7	700	200	4-1/2	007	300 -		400	
8	200	250	4-1/2	700	250	None		
4	350	100	4-1/2	700	150	None		
4	350	100	4-1/2	700	150	2	007	
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TASK 1A - EXHIBIT 2

PROCESSES AND TESTS FOR SILVER (# ALUMINUM) METALLIZATION

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Metal Tem	- tell	emp Time	Time		Amb-	Etchant	Etch	of KPR	Etch		Bonding		Temp	%
(MINS)	(Mins)	(MINS)	+	7	1ent	(11 any)	11me	Kemova	Results	Bonding	Tests Made	Time	_	Bonds
N Type Ag 830 3 Fc Wafer 700 mg ir	830 3	3		Fr iri	Form- ing gas	20% Ferric Nitrate	6 Mins	Chromic Acid	Ag was dis- solved in Chromic Acid					
N Type A1 20 mg 580 3 N <sub>2</sub> Hafer + Ag 700 mg	580 3	580 3		N <sub>2</sub>		20% Ferric Nitrate	6 Mins	Chromic Acid	Ag undercut by Chromic Acid	.002 " Au	Wafer After Alloy	5	345	80
Oxidised Al 20 mg 580 3 N <sub>2</sub> Wafer + Ag 700 mg	20 mg 580 3 + 700 mg	580 3		$^{N}_{2}$		20% Ferric Nitrate	6 Mins	Chromic Acid	Ag undercut by Chromic Acid	.002 " Au	Wafer After Alloy	5	345	80
4200 600 mg See KPR Removal Wafers Ag	See		KPR Removal	val		20% Ferric Nitrate	1m 10s	830°C, 3m in form- ing gas	Some lifting	002 "	After die Attach	5	345	45
4200 600 mg 830 3 Form Wafers Ag gas	830 3	в		Forr		None	•	830°C,3m in form- ing gas	,	.002 " Au	After die Attach Unetched	5	345 1	100
4200 20 mg Al See KPR Removal Wafers + 600 mg Ag	See		KPR Removal	val		20% Ferric Nitrate	30 s	830°C,3m Metal in form- color ing gas Brown	Metal dis- colored Brown	.002 " Au	Wafer After Etch	5	345	10
4200 20 mg Al 830 3 Form Wafers + 800 mg Ag	830 3	3		Forr	g	35% Ferric Nitrate	7 m	830°C, 3m in F.G.	830°C, 3m Metal dis- in F.G. colored Brown	.002 " Au	Wafer After Etch	5	345	20
	830 6	830 6		For	B	20% Ferric	ш 9	Acetone	роод	.002 " Pd	After Die	5	345	0
810 20			70			Nitrate				.002 " Au	Attach	5	345	80
										.001 " Ag		5	345	

Page 2 of 3

TASK 1A - EXHIBIT 2 (Continued)
PROCESSES AND TESTS FOR SILVER (± ALUMINUM) METALLIZATION (Continued)

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	Evap	Metal	and Wt	Ag 700 mg	A1 20 mg + Ag 700 mg	A1 20 mg + Ag 700 mg	600 mg Ag	600 mg Ag	20 <sub>+</sub> mg A1 600 mg Ag	20 mg A1 + 600 mg A	00 €	
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			Substrate	N Type Wafer	N Type Wafer	Oxidised Wafer	4200 Zafers	4200 Vafers	4200 Wafers	4200 Wafers	4200 Wafers	
		Run	*	1	2	m	4	5	9	~	ω	

TASK 1A - EXHIBIT 2 (Continued)

PROCESSES AND TESTS FOR SILVER (\* ALUMINUM) METALLIZATION (Continued)

PROCESSES AND TESTS USED FOR PALLADIUM METALLIZING

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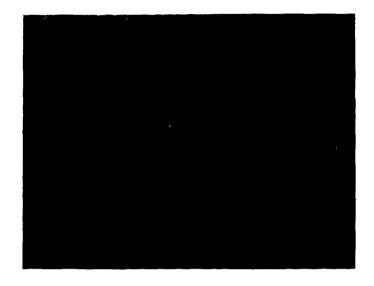
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#### TASK 1A - EXHIBIT 4



Run #34
3000 mg Pd Evaporated
Alloyed 725°C 1/2 Min
in Argon
Magnification x200



Run #36
3000 mg Pd Evaporated
Alloyed 725°C 1 Min
in Argon
Magnification x200

#### TASK 1A - EXHIBIT 5

TABLE SHOWING LONGEST ALLOYING TIMES TO ALLOW PURE PALLADIUM PATTERN SURFACES

AND

SHORTEST ALLOYING TIMES TO PRODUCE PALLADIUM AND SILICON PATTERN SURFACES

VALUES ARE GIVEN AT DIFFERENT ALLOYING TEMPERATURES AND AMOUNTS OF PALLADIUM EVAPORATED.

mg Pd Evaporated	Alloy Temp	Longest All Allow Pure		Shortest All To Produce E	•
'	°C	Time	Run #	Time	Run #
500	730	5 Secs		10 Secs	
1000	600			1-1/2 Mins	
1500	730	15 Secs			
2000	480	3 Mins	İ	20 Mins	
3000	300	2 Hours			
3000	725	30 Secs	<b>!</b>	40 Secs	

#### TASK 1B - SMALL BALL BONDING

#### CONTENTS

Introduction

Narrative and Data

Torch

Capillary

Wire Feed Mechanism

Mechanized Ball Bonder

Program For the Next Interval

Torch

Capillary

Wire Feed Mechanism

Mechanized Ball Bonder

Exhibit 1 - Four Photomicrographs of Titanium Carbide Torch

Exhibit 2 - Results of Tungsten Carbide Capillary Tests

Exhibit 3 - Photograph of Lever Clutch Wire Feed Mechanism

Exhibit 4 - Photograph of Mechanized Ball Bonder

Exhibit 5 - Photograph of Heater Nest

### TASK 1B - SMALL BALL BONDING

D. K. Myers

### INTRODUCTION

A program to improve the lead bonding process has been undertaken by Fairchild Semiconductor. Refinements to the process have been made during the first and second quarter and will continue to be made through the third quarter. Improved reliability is anticipated on gold to aluminum bonding strength, lead wire strength, bond positioning and uniformity of ball size. Improved production rates have also been attained.

The essence of the bonding system is to feed a gold wire through a capillary and form a gold ball at the end of the wire by means of a hydrogen flame. The ball is then positioned and indexed with a rocker arm to a transsistor element previously attached to a header. The header rests in a temperature regulated heater which provides the necessary heat for gold-aluminum thermal compression bonding. Previously, a wedge type bond was used.

A discussion follows which summarizes, by major area of design, the work through the second quarter of the contract and plans for future periods.

NARRATIVE AND DATA

A. Torch: The torch contains and regulates the hydrogen flame which simultaneously cuts the gold wire and forms a ball for the subsequent bonding operation. Currently, a 2.5 mil orifice diameter pyrex glass torch with a radiused exit and controlled speed cam is in use. A titanium carbide torch\* was evaluated and after six hours had to be removed from operation for non-uniform ball size. Figures referred to below may be found in Exhibit 1.

<sup>\*</sup> TPC-T TiC torch from Tempress Research Co., Inc. (Kentanium (R) K-162B extruded tubing) Kentanium is a registered trademark of Kenanetak Inc., Latrobe, Pa.

Figure 1 is a photomicrograph of the exterior of the used TiC torch - note the discoloration when compared to a new torch, see Figure 2. Figure 3 is a photomicrograph of the orifice end of the used torch shown in Figure 1. The non-uniformity of the ball size was caused by the obstruction of the orifice by foreign material and/or the deposition of eroded material and the resultant obstruction of the orifice. This torch is being metallurgically cross-sectioned to ascertain the nature and substance of the obstruction. The new TiC torch orifice is shown in Figure 4 for comparison purposes. The egg-shaped opening is the result of using Kentanium extruded tubing. Extruded tubing was used as opposed to machining from a solid shape because of cost considerations. Torches fabricated with extruded tubing are 1/5 to 1/4 the cost of machined torches.

B. Capillary: The function of the capillary is to contain the gold wire and to act as a pressure bearing surface on the flame-formed ball so that thermal compression between the gold ball and aluminum contact on the transistor element can be effected. Currently, a pyrex glass capillary, 3/4 inch long, with a .0014 to .0016 inch orifice diameter and a slight radius at the orifice is being used in production of small geometry transistors at Fairchild.

Presently, the major problems center around cleanliness of the capillaries and obtaining adequate quality assurance from supplying vendors. Cleanliness of the capillary is an absolute necessity. Dirty capillaries prohibit the threading operation, cause sticking and drag on the gold wire inside the capillary, and reduce the quality of the flame-formed gold ball. Chemical cleaning procedures are being evolved and should help to minimize this problem, but cannot replace good initial quality capillaries from the supplying vendors.

Tungsten carbide capillaries will continue to be evaluated for bonding qualities, life expectancy, and economic aspects. The majority of the work

in this area has been devoted to 2 mil diameter gold wire. The 2 mil wire bonds were evaluated, because a .0025 nominal diameter tungsten carbide capillary was readily available.

Under similar conditions, the tungsten carbide capillary produces a slightly more reliable bond. A summary of the test results follows:

## 78 Bonded with carbide

## 78 Bonded with pyrex

1 Ball off

Base bond at 20 grams

2 Balls off
Emitter bond at 13 grams
Base bond at 18 grams

The failures were determined by pull testing perpendicular to the plane of the transistor element. The magnitude of the force necessary to break the bond or to exceed the tensile strength of the gold wire was recorded (See Exhibit 2). Only those units having a ball bond failure were considered faulty for purposes of this test. Normally a 4 grams force is considered acceptable.

The same tungsten carbide capillary was used for 4 eight hour days before it had to be cleaned. This cleaning operation is purely mechanical as opposed to a costly chemical cleaning for pyrex capillaries. The WC capillary is produced by machining from a solid piece and is inherently more uniform than a pyrex capillary which is pulled from hot tubing. A tool has been designed and constructed which holds the "dirty" capillary on center for the insertion of a cleaning wire to open the orifice. The above tungsten carbide capillary has been in service for over 400 hours of operation and is continuing to provide adequate service.

A preliminary economic analysis indicates that for 50 working days the cost of pyrex capillaries for one machine, 8 hours per day, is \$67.50 (average 8 hour life expectancy) while the cost of the tungsten carbide capillary is approximately \$25.00, resulting in an approximate 50% savings.

C. Wire Feed Mechanism: This mechanism must maintain enough back tension on the gold wire as it is unwound from the spool so that the gold ball is firmly held against the capillary as the capillary is indexed onto and retracted from

the die. Currently, a lever tensioning device is being used which incorporates an offset arm that permits spool unreeling in the forward direction but clutches in the reverse direction retracting the wire by gravitational force. (Exhibit 3) A thin walled spool holder using a washer placed eccentrically around the spool axis to conteract spool imbalance is also being used.

D. Mechanized Ball Bonder: Exhibit 4 shows the production model search bonder. Its design incorporates the refinements resulting from two years of experimentation and evaluation at Fairchild Semiconductor.

Initially, non-uniformity of temperature of the heated headers was causing problems. Exhibit 5 shows the header nest and associate equipment. The problem was minimized by employing a space wound heater element which heats non-uniformly and compensates for the variable distances between the heater and the transistor headers.

### PROGRAM FOR THE NEXT INTERVAL

- A. Torch: The titanium carbide torch tip shown in Figures 2 and 4 of Exhibit 1 will be further evaluated to determine the effect of an oval orifice on flame control and ball size. Further determination will be made of average expected life for the titanium carbide torch.
- B. Capillary: Tungsten carbide capillaries for 1.1 mil diameter gold wire will be evaluated along with continued evaluation for 2 mil diameter gold wire with respect to cost savings, reliability of gold to aluminum bonds, and improving yields. Heating coils for the tungsten carbide capillaries will also be evaluated.
- C. Wire Feed Mechanism: Incorporation of a small felt cleaning pad to clean the wire prior to entering the capillary will be evaluated.
- D. Mechanized Ball Bonder: Heater improvements will be investigated to further improve the thermal characteristics of the heater block.



FIG 1 - Used TiC Torch (42X)

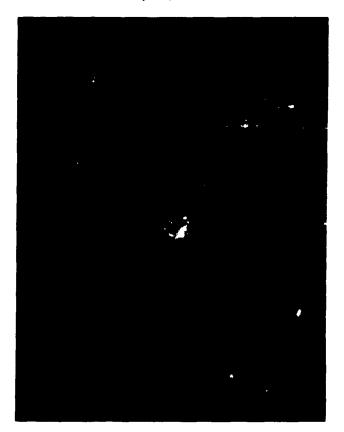


FIG 3 - Used Torch Orifice (213X)

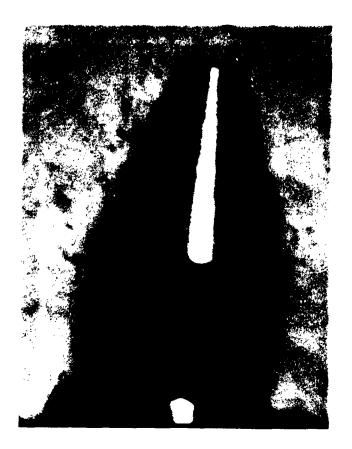


FIG 2 - New TiC Torch (42X)

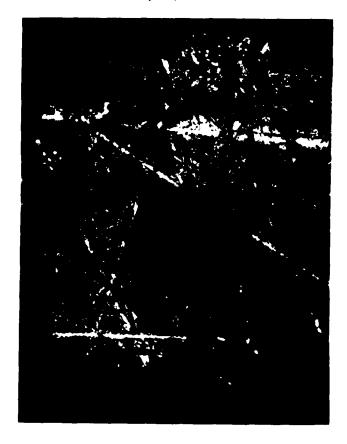


FIG 4 - New Torch Orifice (213X)

Task 1B - Exhibit 1

## TASK 1B - EXHIBIT 2 (Page 1 of 2) RESULTS OF PULL TESTS OF BONDS MADE WITH DIFFERENT CAPILLARIES

CONDITIONS: Tungsten Carbide

Temp.

345°C

Cap Wt.

300 Grams

Bond Time 2 Sec

Test #	Base	Emit.	<u>Test #</u>	Base	Emit.
1	21	26	40	18 *	20
2	23	25	41	18	22
3	22	26	42	20	24
4	22	23	43	23	24
	22	15	44	21	22
6	<b>2</b> 5	26	45	23	25
5 6 7	. 20 **	22	46	25	27
8	22	25	47	28	24
9	23	24	48	25	26
10	26	26	49	20 *	25
11	24	25	50	26	26
12	23	15 *	51	26	27
13	24	22	52	21	23
14	18	20	53	20	26
15	17	25	54	25	24
16	26	25	55	22	19 *
17	24	26	56	22	15
18	22	24	57	24	25
19	25	23	58	27	25
20	25	23	59	22	27
21	22	20	60	24	25
22	24	26	61	25	23
23	25	25	62	25	25
24	26	27	63	19	23
25	26	28	64	20	23
26	26	25	65	23	21
27	2.6 *	21	66	20	24
28	21	26	67	25	24
29	18	27	68	20	25
30	28	32	69	26	27
31	22	27	70	23	25
32	20	26	71	17	17
33	28	23	72	21	25
34	26	26	73	22	20
35	20	25	74	23	25
36	25	25	75	20	20
37	24	26	76	21	25
38	26	22	77	20	23
39	27	27 *	78	24	22

<sup>\*</sup> Rebonds \*\* Ball off

## TASK 1B - EXHIBIT 2 (Page 2 of 2) RESULTS OF PULL TESTS OF BONDS MADE WITH DIFFERENT CAPILLARIES

CONDITIONS:

Glass

345°C

Cap Wt.

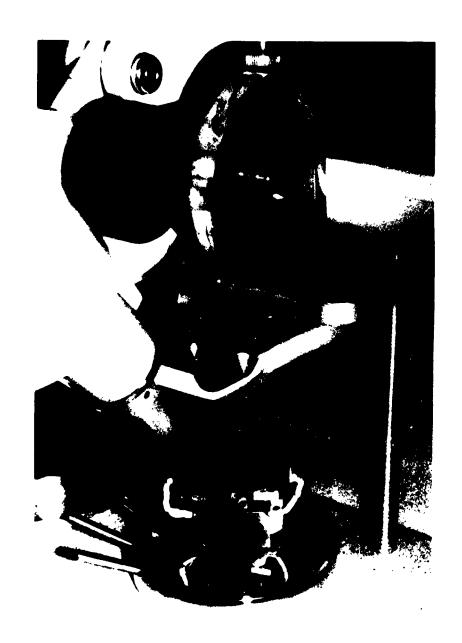
Temp.

300 Grams

Bond Time 2 Sec

Test #	Base	Emit.	<u>Test #</u>	Base	Emit.
1	27 *	25	40	22	23
2	20 *	26	41	14	24
2 3 4	23 Grack	ed 22	42	26	22
4	25	22	43	22	23
5	22	18	44	27	28
5 6 7 8	21	24	45	21	20
7	26	<b>2</b> 5	46	23	19
	15	27	47	26	24
9	27	24	48	22	23
10	25	22	49	27	25
11	<b>2</b> 5	24	50	22	26
12	18 **	25	51	20	<b>2</b> 5
13	<b>27</b>	27	52	26	26
14	22	25	53	24	23
15	21	24	54	22	20
16	24	24	55	19	23
17	24 *	25	56	24	30
18	<b>2</b> 5	<b>22</b>	57	23	22
19	<b>24</b>	28	. 58	22	17
20	16	19	59	24	25
21	17	22	60	21	23
22	15	19	61	20 *	21
23	22	23	62	23	25
24	25	23	63	27	26
25	21	25	64	<b>2</b> 5	26
26	20	<b>25</b> '	65	14	20
27	26	25	66	20	19
28	21	25	67	19	20
29	24	13 **	68	24	23
30	19	19	69	18 *	22
31	23	20	70	22	25
32	17	19	71	22	23
33	22	26	72	22	24
34	21	22	73	23	22
35	22	23	74	27	24
36	26	25	75	24	25
37	18	21	76	27	24
38	23	18	77	21	24
39	20	25	78		

Rebonds \*\* Ball off





MECHANIZED BALL BONDER
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HEATER NEST
III - 1B - 11

# TASK 1C - OPTIMIZE CHARACTERISTICS OF .001 GOLD WIRE FOR CAPILLARY BONDING

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Purpose

Background

Narrative and Data

Conclusions

Program for Next Interval

Exhibit 1 - Table Showing Range of Mechanical Properties of Different Wires

Before and After Bonding

Exhibit 2 - Table Showing Mechanical Properties of Wires Before and
After Capillary Bonding

Exhibit 3 - Graph of Mean Pull Strength vs Annealing Temperature
.001 Gold Wire

## TASK 1C - OPTIMIZE CHARACTERISTICS OF .001 GOLD WIRE FOR CAPILLARY BONDING

### **PURPOSE**

When .001" diameter hard-drawn gold wire is capillary bonded, it is found that a weak point is formed just above the ball bond. Failure is liable to occur at this point during lead welding or environmental testing.

The purpose of this project is to eliminate the weak point above the ball during the bonding of .001" diameter gold wire.

### BACKGROUND

The theory explaining how the weak point is formed above the ball of capillary bonded hard-drawn gold wire is as follows: The wire above the ball is heated by the oxygen flame used to fuse the gold ball. This heating causes annealing and softening of the wire just above the ball. Stresses cause necking and failure of the softened wire in preference to the remaining wire which is hard-drawn.

It was hoped that annealing the gold wire to a softened state prior to capillary bonding would eliminate the weak spot above the gold ball.

Previous work, described in the first report, showed that the strength of bonded, well annealed wire was nearly the same as that of annealed wire prior to bonding. This supported the theory that the formation of the weak spot could perhaps be eliminated by giving the correct annealing treatment to the hard-drawn gold wire.

Some annealed wire with a tensile strength of less than 20,000 psi and % elongation greater than 12% was ordered from two vendors since it was hoped that wire in this highly annealed condition would have desirable bonding properties. Tensile and elongation tests were performed on the received wire.

#### NARRATIVE AND DATA

Annealing tests were performed on hard-drawn .001" diameter gold wire supplied by Western Gold and Platinum Co., Belmont, California. The spool of hard-drawn wire to be annealed was mounted vertically between bearings so that the spool could rotate freely. The wire was passed through a 750 watt Multiple Unit Hevi Duty Electric Co. tube furnace resting in the horizontal position. The end of the wire was attached to a spool clamped to a shaft. A geared chain drive, connected to a Variac controlled Bodine Electric Co. variable speed motor, was used to rotate the shaft and pull the wire through the furnace. This apparatus allowed the wire to be annealed at variable rates and at variable temperatures.

Initial tests involved annealing samples of the hard-drawn wire at a fixed rate and at different temperatures. It was hoped that the results of these tests would indicate whether further tests were necessary, either at annealing temperatures other than those initially tried or at different rates.

The tensile strength in grams and the elongation over a two inch length were measured for hard-drawn wire and for wires with different annealing schedules. The measurements were made using an Instron Tensile Tester.

Two or more tests were made on wire samples from each spool of wire. Capillary bonds were made to dice with aluminum patterns using the differently heat treated wires. Two methods were used to evaluate the pull strengths of the bonded wires.

- (1) The bonded wire was pulled with tweezers until fracture, the mounted die being clamped to the measuring arm of a dynamometer. The dynamometer pointer indicated the breaking strength of the wire.
- (2) The bonded die, mounted on a header, was clamped in one of the jaws of the Instron Tester. The end of the wire was clamped in the other jaw. A pull test was then performed.

The Instron Tester being a much more sensitive piece of equipment than the dynamometer, it may be assumed that the results obtained by method (2) were more accurate than those obtained by method (1).

A table was drawn up showing the results of the mechanical tests made to the various wires; it will be found as exhibit 1 at the end of this report. Results are shown for hard-drawn wire, for wire annealed to specification by Secon Corp. and Wesgo Co., and for wire annealed here with the apparatus described. The table shows the pull strengths of the wires in the unbonded and the bonded conditions, and the number of tests made. The elongation of the unbonded wire is also shown. Two values are given for the pull strengths of unbonded wires in cases where two separate annealed spools were made at the same alloying schedule. Some blank spaces remain on the table in places where mechanical tests have not yet been performed on the annealed wires.

Another table was made showing the range of pull strengths and elongations which were obtained for wires given different annealing schedules. This will be found as exhibit 2 at the end of this report. The table shows that the range of pull strength and elongation values obtained for any given spool of annealed or un-annealed wires is quite narrow. The fact that the annealed wires displayed uniform mechanical properties within a spool suggests that the apparatus used for annealing is a satisfactory one. The range of pull strengths that occurs on pulling bonded wires from any given spool is much larger. This can partially be explained by the fact that many more tests were used in evaluating a bonded wire than an unbonded wire; it is obvious that the more tests are made the larger the range of values will tend to become. However, it does appear that reproducibility of capillary bonding results is difficult and that large numbers of tests must be made to give statistical evidence from which trends may be seen.

The two tables show that very different mechanical properties were obtained when tests were made on different spools which had supposedly received identical treatment. The difference in mechanical properties of the two randomly selected hard-drawn spools is especially noteworthy. This means that care must be taken in selecting spools of hard-drawn wire with similar mechanical properties for future annealing tests.

It is difficult to draw any valid conclusions from the results available at this time, since more tests must be made. A graph of mean pull strength versus annealing temperature was plotted and will be found as exhibit 3 at the end of this report. The graph is not completely accurate owing to the small test samples. The hard-drawn wire was plotted, for convenience sake, as having been annealed at 25°C. It can be seen that the pull strength of the annealed wire decreased with increasing annealing temperature, as was expected. The tensile strength of the bonded wire remained fairly constant with temperature. At about 500°C the strength of the bonded wire became very similar to that of the unbonded wire.

### CONCLUSIONS

Annealing the wire at 500°C at a pull rate through the furnace of 13" per minute caused the bonded and unbonded wires to have very similar properties, thus reducing the occurrence of a weak spot above the ball. Further annealing and mechanical tests must be performed before the exact annealing conditions needed to eliminate the weak point can be found.

### PROGRAM FOR NEXT INTERVAL

Further annealing tests will be carried out in order that an annealing schedule may be found which will eliminate the weak point formed above the gold ball in thermal compression bonding.

Metallographic inspection of bonded and unbonded wires will be undertaken. Attempts will be made to observe the deformation and necking that occurs when failure occurs in the region of the gold ball.

TASK 1C - EXHIBIT 1

TABLE SHOWING RANGE OF MECHANICAL PROPERTIES OF DIFFERENT WIRES BEFORE AND AFTER BONDING

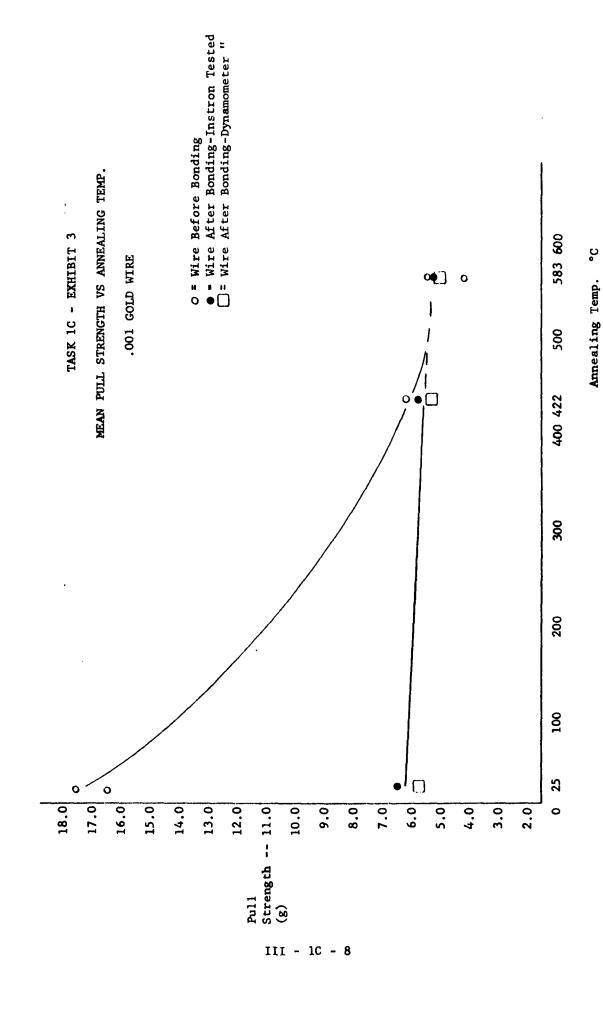
		Befor	Before Bonding		Bond	Bonded Pull Strength	trength	
					Dynamometer	meter	Instron	uo
		Number	Pull Strength		Pull Strength	Number	Pull Strenoth	Nimbor
Wire Vendor	Annealed Condition	of Pulls	(g) Range	E1 % Range	(g) Range	of Pulls	(g) Range	of Pulls
Wesgo	Hard-Drawn	. 7	۲. ون	.05	3.7	16	3.8	<b>®</b>
Secon	As received FTU < 20,000 psi % E1 > 12%	2	۳.	1.0				
Wesgo	As received FTU < 20,000 psi 7 El > 127	2	ε.	0				
Wesgo	Annealed 583°C	7	1.2	1.0	2.5	10	7.	6
Wesgo	Annealed 422°C	7	7.	3.5	2.3	13	1.0	10

TASK 1C - EXHIBIT 2

TABLE SHOWING MECHANICAL PROPERTIES OF WIRES BEFORE AND AFTER CAPILLARY BONDING

		Pu11 Pu11	Pull Tests (Instron) Before Bond Pull Strength	ron) Be	fore		Sonded Pull Tests (Dynamometer) Number (Pull Stren	Pull momet	th	Bonded Pull Tests (Instron) Number Pull Stren	ed Pull T (Instron) er Pull S	Tests ) Strength
Wire	Annealed Condition	(g) Mean		No. of % Elongn Pulls Mean Range	% Elo Mean		Bonds Pulled Mean	Mean	(g) Range	Bonds Pulled Mean	Mean	(g) Range
.001" Wesgo Hard-Drawn		16.8 17.6	16.5-17.2	2	.42	.4045	16	5.84	4.0-7.7	8	6.4	3.8-7.6
.001" Secon	.001" Secon As per spec E1 > 12% UTS 20,000 psi	7.2	7.1- 7.4	2	9.0	8.5-9.5						
.001" Wesgo	=	8.3	8.2- 8.5	2	12	12-12						
.001" Wesgo	" 583°C	4.3 5.9	4.3- 4.3 5.2- 6.4	2 4	6.5 9.1	6- 7 9.0-9.5	10	5.7	4.5-7.0	6	5.8	5.4-6.1
.001" Wesgo	" 422°C	6.3	5.9- 6.6	4	9.0	7.5-11.0	13	5.13	4.7-7.0	10	6.7	6.1-7.1
.001" Wesgo	ɔ。009 ,,											
.001" Wesgo	" 485°C									-	,	
.001" Wesgo	" 538°C											

Rate of Pulling Wire Through Furnace = 13"/Min 3onds Were Made to the Emitter Dots of 4200 Units.



## TASK 1D - ULTRASONIC BONDING

## CONTENTS

Purpose

Background

Progress During This Period

Conclusions

Program

Exhibit 1 - Dynamometer Pull Test Results

Exhibit 2 - Sketch of Mounting for Transducer- Coupler Assembly

### TASK 1D - ULTRASONIC BONDING

W. J. Warren

### **PURPOSE**

The purpose of this work is to develop a method of bonding pattern metal to lead wire by means other than thermal compression. The method should be insensitive to the surface condition of the metals being joined and should not have a critical bonding temperature.

Ultrasonic welding fulfills the above criteria and has been chosen for investigation.

### BACKGROUND

In the first period ultrasonic bonds were made between .001" aluminum 1% Si wire and aluminized dice. However, the bonds made were not of reproducible high mechanical strength.

### PROGRESS DURING THIS PERIOD

A new, more sophisticated assembly was received during this period for housing the transducer coupling assembly and holding the sonotrode, and was found to operate satisfactorily. (See exhibit 2)

Some of the .001" diameter aluminum 1% silicon wire was annealed by pulling it through a tube furnace with a hot zone temperature of 500°C; the rate of pulling was 13" per minute. This softened the wire considerably.

As mentioned in the last report

 $H^{3/2}T^{3/2}$  = KE(according to verbal communication from Sonobond Borp.)

- where H hardness of the wire (Vickers)
  - T thickness
  - K constant
  - E energy for ultrasonic bonding.

Annealing the wire should therefore reduce the energy necessary for ultrasonic welding.

Some bonds were made to small geometry units which were then given standard in-plant centrifuge tests at 40,000 g's, two positions for two minutes.

It can be seen from the results given in Exhibit 1 that some bond failures occurred owing to stresses involved in bending the bonded wires for lead welding. The cans were cut off the units after centrifuging and the units visually inspected for bond failure. No failures due to acceleration were observed, showing that the bonds were of considerable mechanical strength. However, it would be desirable to produce bonds of sufficient strength that they would all survive lead welding.

It was decided to introduce the dynamometer pull test as a means of evaluation of ultrasonic bond strength. The pull strength of a bonded unit was found by clamping a lead bonded, but not welded, unit to the dynamometer measuring arm. The bonded wire was then pulled with tweezers in the direction of rotation of the measuring arm until bond or wire failure occurred. The dynamometer dial registered the breakage strength. For the tests described below the units were clamped so that the wires were pulled in a direction in line with the bond and parallel to the header surface.

Bonds were made to large geometry units (as a test bed due to the convenience of large bond area) with both unannealed and annealed .001" AlSi 1% wires. The settings used were those reported last period to be optimum for the bonding of hard drawn aluminum wire. Several different types of sonotrodes were used so that their effect on the pull strength of ultrasonic bonds might be determined. It was found that bonds could be made at least 90% of the time with both unannealed and annealed wires and with all the different sonotrodes. It was found, in contradiction to that reported in the previous period, that ungrooved sonotrodes were satisfactory, provided care was taken to make the sonotrode face and die parallel. The results of dynamometer pull tests using different sonotrodes, annealed and unannealed wires, are shown in exhibit 1. When the pull tests were performed, it was noted whether bond failure occurred in the span of the wire, at the bond interface, or at the angle between the bonded and unbonded wire. This latter region was particularly liable to failure owing to the sudden change

in wire cross section at this point, and also because any movement in the wire caused fatigue at this point. The same failure mechanism is liable to occur in thermal compression wedge bonds.

A mean pull strength of 3.8 grams has been obtained for wedge bonds of .001" AlSi 1% to ten small geometry units. Each of the four ultrasonic bonding tests gave higher mean pull strengths than were obtained for the thermal compression wedge bonds. Other interesting results may be seen from the tables at the end of this report. Very few failures occurred at the bond itself in any of the tests. It may be seen that the mean pull strength of those tests which used hard drawn wire was about four times higher than those using the annealed wire. The tensile strength of the wire was reduced by the annealing treatment to such an extent that in-span failure was easily the most common failure mode. However, where hard drawn wire was used most failures occurred at the angle between bonded and unbonded wire. It was not possible to discover from the results whether the annealed or hard-drawn wire gave stronger ultrasonic bonds, since bond failure at the bonded interface was so infrequent.

It seemed possible to conclude that ultrasonic bonds were stronger than wedge bonds. Also, that hard drawn wire was preferable to the highly annealed wire used in other tests.

CONCLUSIONS Ultrasonic bonds can be produced between .001" AlSi 1% wire and and aluminized dice which are stronger than thermal compression wedge bonds.

It is not yet known whether annealed or hard-drawn wire gives stronger bonds at the wire-dice interface. However, highly annealed wire is weaker in the span of the wire than at the bond; therefore, hard-drawn wire is preferable to highly-annealed wire.

### PROGRAM FOR NEXT INTERVAL

Different annealing schedules will be tried to see if less than fully annealed wire will give improved bonding characteristics.

A large number of ultrasonic bonds will be made in order that further strengths and in-plant environmental tests may be performed.

TASK 1D - EXHIBIT 1 DYNAMOMETER PULL TEST RESULTS

			Broke	Broke in Span		Brok Bon	Broke at Angle Between Bonded and Span Wire	gle Betw Span Wi	een re		Broke at Bond	t Bond		Total	al
		Emitter	Į,	Base	ie	Emitter	L	Base	e e	Emitter	1	Base	e		
Type of Sonotrode	Type of Wire	Number	Mumber Pull(g) Number	Number		Number	MeanMeanMeanMeanMeanMeanPull(g)NumberPull(g)NumberPull(g)NumberPull(g)	Number	Mean Pull(g)	Number	Mean Pull(g)	Number	Mean Pull(g)	Number	Mean Pull(g)
Grooved Wedge (F*.005)	Annealed	7	4.6	9 .		7	3.5	2	2.5	0	ı	0	1	19	4.1
Grooved Wedge (F*.002)	Hard Drawn	0	1	2	19.5	10	16.1	8	16.3	0	,	0	b	20	16.5
Ungrooved	Ingrooved Annealed	ω	4.0	11	4.4	1	2.5	1	3.8	1	1.5	0	•	22	4.0
Ungrooved Hard Wedge Drawn	Hard Drawn	-	18.5	2	21.0	6	19.3	6	18.7	0	ı	2	9.5	23	18.3
											Span	7			

Settings Used to Make Bonds.

Load applied to Sonotrode tip = 250 on sliding scale Sonoweld Model No. W-20-TSL settings:
Resonance setting = +30
Power = 0.4 watts
Time = 1.5 secs.

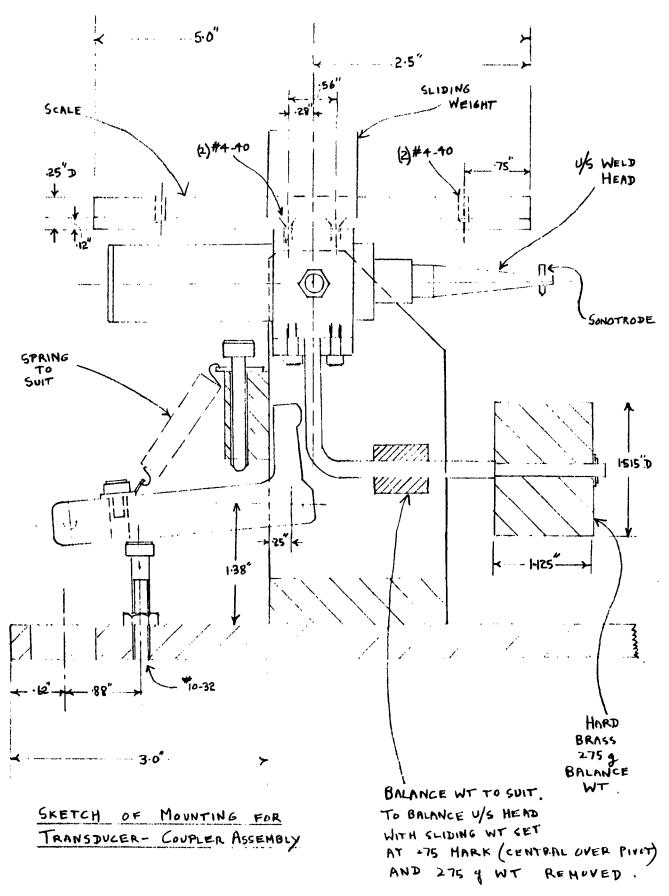
Dynamometer Pull Direction

Die

Angle

Bond

III - 1D - 6



TASK 1D - EXHIBIT 2

## TASK 1E - DYNAMIC AND STEP STRESS TESTING

## CONTENTS

Summary of Work in This Period

Dynamic Testing - Narrative and Data

Thermal Shock

Conclusion

Constant Acceleration (Centrifuge)

Shock

Vibration

Conclusion

Step Stress Program - Narrative and Data

Summary of Deviations From the Program Outline

Exhibit 1 - Thermal Shock Results

Exhibit 2 - Vibration Test Results

Exhibit 3 - Step Stress Program Outline

#### TASK 1E-DYNAMIC AND STEP STRESS TESTING

J. A. Corzine

## SUMMARY OF WORK IN THIS PERIOD

Thermal Shock and Vibration testing were tentatively completed this period. Shock testing is awaiting the "debugging" and Acceptance Performance Testing of the prototype Avco Shock Tower recently delivered. Phase II of Constant Acceleration testing is now underway after redesign of inserts for the large sample size rotor. The Step-stress Program is in progress with little difficulty encountered. DYNAMIC TESTING - NARRATIVE AND DATA

### Thermal Shock

The thermal shock portion of this contract was completed during this quarter. Five failures were encountered from a sample size of 100 units. All failures occurred in the first 25 cycles of this test. It is significant to note that if a unit withstands 25 thermal shock cycles between -65°C and +150°C, this test indicates that it will survive prolonged temperature cycling. These units underwent a total of 70 hours of thermal cycle temperatures.

As discussed in the last quarterly report, a great number of difficulties were encountered with this portion of the contract. We reported that an ink had been found to serialize the units that would withstand the attack of the cold liquid (Trichlorethylene). In the process of completing the last period of thermal shock (400 cycles) the serial numbers were removed by the liquid cooling medium. In short tests, the ink had proven satisfactory; however, due to the long exposure to the trichlorethylene during the 400 cycles, the identity of individual units was lost making a final parameter readout of little value.

Final parameter testing did indicate that  $h_{FE}$  does degrade to some extent under prolonged thermal shock conditions. Assumptions only can be made from the parameter data compiled; however, average  $h_{FF}$  value for the sample had

degraded approximately 20% between initial and post testing. This subject will be discussed below in the Deviations From Program Outline section of this report.

Conclusion: Thermal shock when conducted between the temperatures -65°C and +150°C with units experiencing a time at room temperature between immersions no longer than 30 seconds is a practical, expedient method of determining which devices are not representative of the true manufacturing process. This test may provide a failure rate of suitable magnitude that a statistical analysis can be applied to comparisons of production processes or competitive manufacturer's products. The results of this test corroborate the fact that this test need not be of a large magnitude. The results of 25 thermal cycles provided as much data as did the full 800 cycles of this test. In fact, a test shorter than 25 cycles may provide as much data as can be obtained from large number of cycles. Results of thermal shock tests are presented in exhibit 1.

### Constant Acceleration (Centrifuge)

The centrifuge rotor used for "G" levels between 50,000 and 150,000 "G's" for sample sizes larger than six units was not equipped with inserts to accept TO-18 size units at the initiation of this Program. An experiment was conducted into the feasibility of Teflon as a material for use as rotor inserts. A series of tests was made after Teflon inserts were designed and manufactured for this rotor. The tests indicated that after a few minutes at 100,000 "G's" the inserts became plastic and malleable even to the point of allowing one unit to pass through the insert and lodge against the rotor wall.

Inserts of aluminum have been designed and tested. The delivery of these aluminum inserts has delayed Phase II of this portion. These inserts are now in use and centrifuging for Phase II is now underway.

## Shock

The Prototype Shock Tower was delivered this quarter. Testing of the tower revealed that the piston and cylinder assembly were not operating to design specifications. Replacement of the cylinder assembly disclosed the original

unit had failed due to the many hours of testing it had undergone before delivery. The tower then met design requirements, but another problem was encountered. At present this shock tower does not possess the repeatability deemed necessary for a controlled experiment. "G" levels will vary 15% with all controls remaining constant: Intensive effort is being applied to overcome this problem. Preliminary shock testing has indicated that the area of interest for these tests is in the 10,000 to 15,000 "G" level range. This information is in conflict with the 5,000 "G" level originally estimated. Changes to the Program outline to investigate this range are discussed below.

#### Vibration

Due to mass limitations of the shaker table of our vibration machine, the units for vibration were divided into three groups; 216 units were mounted in a cube (36 units in each orientation) and vibrated for 96 hours at the "G" level outlined in our original plan. No failures were encountered.

Since the drafting of our plan, Fairchild Semiconductor has purchased an Unholtz-Dickie Sinusoidal-Complex-Random Vibration machine. This new machine was built to Fairchild specifications incorporating the facility of programming random vibration on top of the sinusoidal wave with "G" levels ten times those outlined in the original plan. Since our primary objective was to produce a failure rate from which devices could be evaluated, and no failures resulted from the testing of the first group, we decided to run the second test group at the maximum capabilities of the new vibration equipment.

The second group (216 units, 36 in each orientation) underwent 96 hours of sinusoidal vibration with Gaussian random vibration programmed on top of the sinusoidal wave. The sinusoidal vibration frequency was varied logarithmically between 20 and 2,000 cps. with a sweep time of ten minutes. Amplitude at maximum peak acceleration was 46 "G". This test produced no failures.

Conclusion: Vibration testing is not an expedient means of evaluating silicon planar epitaxial transistors. The equipment available in the vibration field is not destructive to this type device.

We do not plan to test the remaining 168 units. Results of vibration tests are presented in exhibit 2.

## STEP-STRESS PROGRAM - NARRATIVE AND DATA

The Step-stress Program is in progress at the present time. Fairchild had completed a Step-stress Program on a similar device immediately prior to the start of this contract. Some valuable data were obtained from the prior test. These test results indicated that one-hour, three-hour and nine-hour increments of time-under-stress are not of sufficient length to allow operating power or storage temperature to effect a silicon device. Temperatures far in excess of the die-attach temperatures used on the production line were applied before failures were encountered. Engineering judgment dictated that the approach to testing under this contract be modified to obtain the objective of this test. Time periods of 9, 27 and 81 hours were determined to be of enough duration to allow the devices to stabilize at the power and temperature levels applied. This program is being conducted on these intervals.

A visit to Fairchild by technical representatives of the Army Electronic Materiel Agency resulted in changes in this program. The representatives believed that the stress levels of the operating power portion of this program were not high enough to provide data expediently. It was decided that a large increase should be made from the 480 mW that we were then testing at to 600 mW on the 81-hour testing. The accompanying chart reflects this change. (Exhibit 3). It was also decided that 27-hour testing should be started immediately at 600 mW rather than waiting for a failure rate to develop on the long-term tests to indicate the stress level of interest, and the 9-hour tests would begin at 600 mW also. The size of increases of power in the steps was also changed to 40 mW. The storage temperature portion was verified as having reached the area of interest; therefore no changes other than the time increments were made from the original proposal.

Exhibit 3 outlines the Step-stress Program. Progress to date is also noted on the exhibit.

## SUMMARY OF DEVIATIONS FROM THE PROGRAM OUTLINE

- Vibration testing has been terminated after testing was completed on 432 units of a total sample of 600, because no failure rate was established even after the stress level was raised on 216 of these units by a power of ten.
- 2. Shock testing will be raised from 5,000 "G's" to 15,000 "G's" with a pulse duration of .1 millisecond. Acceptance tests of the new shock tower using similar units indicate the area of interest for shock testing is between 10,000 and 15,000 "G's". Tests will be conducted starting at 15,000 "G's" and decreasing in steps of 1,000 "G's". Procedure will remain as outlined in the original plan, only the stress levels are changed.
- 3. Time-under-stress periods for the Step-stress Program will be changed from 1, 3 and 9-hour tests to 9, 27 and 81 hour intervals. All of the above changes are discussed above in the narrative portion of this report.
- 4. Results from the Thermal Shock testing indicate a need for breaking the first 25-cycle test into smaller increments to determine if the failure rate established from the data compiled from the 25-cycle test might not have been, in fact, from the first five or ten cycles of this test. We plan, when this portion is rerun on the improved device, to use 5-cycle intervals until 25 cycles are reached in order to verify this hypothesis. We also plan to lower the maximum number of cycles to 100 because no usable data was generated on the first test beyond 25 cycles.

## TASK 1E - EXHIBIT 1 SIGNAL CORPS CONTRACT

## Start Date Completion Date

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## Thermal Shock

Type 2N914

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## TASK 1E - EXHIBIT 1 SIGNAL CORPS CONTRACT

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Page 2 of 2 Start Date Completion Date

# Thermal Shock Type 2N914

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## TASK 1E - EXHIBIT 2 SIGNAL CORPS CONTRACT

Page 1 of 9	)
Start Date	
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## Vibration Test

## Type 2N914

Unit	20-2000-20	Period I	Period II	Period III	Period IV	Period V	Period VI
No.	Cycle Time	3 Hrs	3 Hrs	6 Hrs	12 Hrs	24 Hrs	48 Hrs
4001	10 Min.	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good
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4051	11	11	11	11	11		
4052	††	11	11	11	11	11	"
4053	11	11				···· · · · · · · · · · · · · · · · · ·	
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## TASK 1E - EXHIBIT 2 SIGNAL CORPS CONTRACT

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Completion Date

Vibration Test

## Type 2N914

Unit	20-2000-20	Period I	Period II	Period III	Period IV	Period V	Period VI
No.	Cycle Time	3 Hrs	3 Hrs	6 Hrs	12 Hrs	24 Hrs	48 Hrs
4054	10 Min.	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good
4055	11	11	11	11	11	- 11	11
4056	- 11	11	11	11	11	11	"
4057	11	11	11	11	H	- 11	11
4058		11	11 -	11	11		"
4059	11	"				11	<u>"</u>
4060	11	11	11	11	"	- 11	"
4061	····	<del> </del>	"	11	11	11	"
4062	- 11	11	!'	11	11	"	"
4063 4064		"		"	11	H	"
4065	11	11	<u>"</u>	11	11	<u>"</u>	<del>                                     </del>
4066		<del> </del>	11	<u>''</u>	"	"	11
4067		<del> </del>	"	<del></del>	11	<del></del>	<del></del>
4068	11	<del> </del>	11	11	<del></del>	<del></del>	<del>'''</del>
4069	11	11	11	11		11	<del> </del>
4070	11	"	"	11	11	<u>'</u>	<del>                                     </del>
4071	11	11	t1	11	11	11	"
4072	11	"	11	11	11	***	11
4073	11	"	11	11	11	11	11
4074	11	11	11	11	11	11	11
4075	11	11	11	11	11	11	11
4076	11	11	11	11	11	*11	11
4077	11	11	11	11	11	11	11
4078	11	11	11	11	11	11	11
4079		11	11	11	11	11	11
4080	11	11		. 11	11	11	11
4081	II		- 11	11	11		11
4082			!!	- 11	11	11	11
4083		11	!!		- 11	- 11	11
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4092	11	11	"	11			***
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4094	"	11	11	11	<u>;;</u>		<del>'''</del>
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4097	11	11		11	11	11	11
4098	11	ti	11		11	11	11
4099	11	11	11	11	11	11	11
4100	†1	11	11	11	11	11	
4101	11	11		11	()	11	11
4102	!!	11	11	11			
4103	II .	11	11	11	11	11	11
4104	11	11		11	11	"	
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4106	11	11					·

## TASK 1E - EXHIBIT 2 SIGNAL CORPS CONTRACT

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Vibration Test

## 2N914

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4112	71
4113         """"""""""""""""""""""""""""""""""""	91
4114         "	
4115         """"""""""""""""""""""""""""""""""""	11
4116         II         I	11
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4118       "	
4119       """"""""""""""""""""""""""""""""""""	11
4120       n	<del></del>
4121       """"""""""""""""""""""""""""""""""""	
4122       """"""""""""""""""""""""""""""""""""	11
4123       II       <	11
4124       II       <	11
4125     """"""""""""""""""""""""""""""""""""	<u>''</u>
4126     """"""""""""""""""""""""""""""""""""	
4127       """"""""""""""""""""""""""""""""""""	11
4128     II     II     II     II     II     II       4129     II     II     II     II     II     II       4130     II     II     II     II     II     II       4131     II     II     II     II     II     II       4132     II     II     II     II     II     II       4133     II     II     II     II     II     II       4134     II     II     II     II     II     II	<u>''</u>
4129     """"""""""""""""""""""""""""""""""""	
4130     """"""""""""""""""""""""""""""""""""	11
4131     """"""""""""""""""""""""""""""""""""	<u>''</u>
4132     II     II     II     II     II       4133     II     II     II     II     II       4134     II     II     II     II     II       4135     II     II     II     II     II	
4133     II     II     II     II     II     II       4134     II     II     II     II     II     II       4135     II     II     II     II     II     II	11
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	11 11 11 11 11 11 11 11 11 11 11 11 11

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Vibration Test

Type 2N914									
Unit	20-2000-20	Period I	Period II	Period III	Period IV	Period V	Period VI		
No.	Cycle Time	3 Hrs	3 Hrs	6 Hrs	12 Hrs	24 Hrs	48 llrs		
4160	10 Min.	Unit Good	Unit Good	<del></del>	Unit Good	Unit Good	Unit Good		
4161	11	"	"	11	"	11	"		
4162	11	"	11	11	11	11	11		
4163	11	11	11	11	11	11	31		
4164	11	tt .	11	11	11	11	11		
4165	11	11	11	H	11	11	11		
4166		"	11	11	11	"	11		
4167	11	11	"	11	11	- 11	<u>"</u>		
4168	H	11	11	11	11		<del>"</del>		
4169	11	11		11	11	11	"		
4170	11	11	11	11	11	11	<del> :</del>		
4171	11		···············		<del> </del>	" "	"		
4172 4173	11	<u></u>	<del></del>	<del></del>	<del></del>	<del></del>	<del> ;;</del>		
4174	11	····	ļ	11	<u></u>	11	<u>"</u>		
4175	11	11	11	"	11		1 "		
4176	11	11	"	11	11	11	,,,		
4177	11	"	11	11	11	11	11		
4178	11	11	11	11	11_	11	11		
4179	11	11	11	11	н	11	11		
4180	11	11	11	11	11		11		
4181	11	"		11	!!		<u>"</u>		
4182	11	<u>"</u>		!!			"		
4183		"	"				<u>"</u>		
4184 4185	<u>f1</u>			11	- 11		<del>  "</del>		
4186	11	11				11	<del> </del>		
4187		<del></del>	11	. 11	11		<del>  - "</del>		
4188	11	<del>"</del>	11	,,,	<del>  </del>	11	"		
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4190	11	· "	11	11	11	11	"		
4191	11	11	. 11	11	řI	11	<del></del>		
4192	*1	11	11	11	11	11	11		
4193	11	11	11	11	61	11	11		
4194		11	11	11	11	11	"		
4195	!!	11	11	11	11	11	- "		
4196	11	11	11	!			11		
4197	••	- "	- "	11			<u>"</u>		
4198	11			11		!!	<u>"</u>		
4199 4200	·		1) 	†1 	11		ļ <u>"</u>		
4200					11		<del>  - !!</del>		
4202	11		11		11	- · · · · · · · · · · · · · · · · · · ·	<del> </del>		
4203	11	11	11	<u></u>	11		<del></del>		
4204	11	!!	11	11	11		<del>                                     </del>		
4205	11	11	11	11	11	91	11		
4206	11	11	11	11			11		
4207	11	11	11	11	11	. "	11		
4208	"	11	11	11			11		
4209	11	11	"	11	11	11	11		
4210	11	11	11	#1	11				
4211	11	11	11	11	11	11	"		
4212	11	11	11	11	11	11	11		
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Vibration Test

Unit	20-2000-20	Period I	Period II	Period III	Period IV	Period V	Period V
No.	Cycle Time	3 Hrs	3 Hrs	6 Hrs	12 Hrs	24 Hrs	48 Hrs
4213	10 Min.	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good
4214	11	11	11	11	11	H	11
4215	11	"	11	11	71	11	11
4216	11	11	11	11	11	91	11
4217	11	11	11 ·	11	11	11	11
4218	11	11	11	11	11	11	11
4219	11	11	11	11	11	11	11
4220	tt	11	11	11	11	11	11
4221	11	11	- 11	11	11	11	11
4222		11	- 11	11	- "	11	11
4223	11	"	11	11	*1	11	"
4224		"	"	11	*1	11	"
4225	11	<u> </u>	11	11	11	11	11
4226	11	11	11	11		11	11
4227	<u> </u>	!!	11	11	11	11	"
4228		11		11	11	11	"
4229	- !!	11	11	"			"
4230	- 11	11	11	!1		11	<del> "</del>
4231		***	- 11				"
4232		"		11		11	11
4233	<del>!!</del>	!!		- 11		!!	
4234		!!	11	11	11	11	"
42 35	11	"	11	- 11	- 11	11	11
4237	11	11	11	11	11	11	
4238	11	<u>"</u>	!!		11	"	11
4239		11	11	. "	11	11	<del>  "</del>
4240	11	''	"		11		<del>  "</del>
4241	11	11	11	<del></del>		11	<del>                                     </del>
4242	11	11	11	<del></del>	11	<del></del>	<del> </del>
4243	11	11		11	11	11	<del></del>
4244	11	11	11	11	11	11	<del> </del>
4245	11	11	11	11	11	11	11
4246	11	11	11	11	11	11	<del>                                     </del>
4247	11	11	11	11	11	11	11
4248	11	11	11	11	11	11	11
4249	11	11	11	11	"	11	<del> </del>
4250	11	11	11	11	11	11	"
4251	11	11	11	11	11	11	"
4252	11	11	11	11	11	11	"
4253	11	11	11	11	11	11	"
4254	11	11	11	11	11	11	"
4255	- 11	11	11	11	11	11	11
4256		11	11	11	11	11	17
4257		11	11	11	11	11	11
4258	11	11	11	11	11	11	11
4259		11		- 11	11	11	11
4260	11	11	11	11	11	11	11
4261		11				11	11
4262	11	11				11	- 11
4263		11	11	11	11	11	
4264	11	"	.,	11	11	11	<u> </u>
4265	11	11			11	!!	,,
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# Vibration Test

No.         Cycle Time         3 Hrs         3 Hrs         6 Hrs         12 Hrs         24 Hrs           4266         10 Min         Unit Good         Unit Good<	11 11 11 11 11 11 11 11 11 11 11 11 11
4267     """"""""""""""""""""""""""""""""""""	11 11 11 11 11 11 11 11 11 11 11 11 11
4268     """"""""""""""""""""""""""""""""""""	11 11 11 11 11 11 11 11 11 11 11 11 11
4269     II     II     II     II     II       4270     II     II     II     II     II       4271     II     II     II     II     II       4272     II     II     II     II     II       4273     II     II     II     II     II       4274     II     II     II     II     II       4275     II     II     II     II     II	11 11 11 11 11 11 11
4270     II     II     II     II     II       4271     II     II     II     II     II       4272     II     II     II     II     II       4273     II     II     II     II     II       4274     II     II     II     II     II       4275     II     II     II     II     II	11 11 11 11 11 11 11 11 11 11 11 11 11
4271     II     II     II     II     II     II       4272     II     II     II     II     II     II       4273     II     II     II     II     II     II       4274     II     II     II     II     II     II       4275     II     II     II     II     II     II	11 11 11 11 11 11
4272     II     II     II     II     II       4273     II     II     II     II     II       4274     II     II     II     II     II       4275     II     II     II     II     II	11 11 11 11 11
4273     11     11     11     11     11       4274     11     11     11     11     11     11       4275     11     11     11     11     11     11	11 11 11
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4316 " " " " "	
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4310	- 12

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Vibration Test

Unit No.	20-2000-20 Cycle Time	Period I 3 Hrs	Period II 3 Hrs	Period III 6 Nrs	Period IV 12 Hrs	Period V 24 Hrs	Period V 48 Hrs
4319	10 Min.	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good
4320	11	11	11	*1	"	11	*1
4321	11	11	11	*1	11	11	11
4322	11	11	11	11	11	11	11
4323	11	11	"	11	11	11	" "
4324	11	11	11	11	11	11	11
4325	11	11	11	11	11	11	11
4326		11	"	11	11	11	<u> </u>
4327		11	11	11	11	"	11
4328		"	"		11	11	11
4329	<u>!!</u>	11		11	11	<u>"</u>	11
4330	<u> </u>	11	11	11	11		11
4331		11	11	11	11		"
4332		11	11	11	<u>+1</u>		<u> </u>
4333		"		11	11		"
4334	11	11	11	11	11	11	!!
4335	11	<del>  ''</del>	11	11	11	11	11
4336		" "	· · · · · · · · · · · · · · · · · · ·	11	11	11	"
4337	11	<del>                                     </del>	11	1,	11	11	<del>  "</del>
4338	······································	<del>  "</del>	11	<del></del>		11	1 11
4339		<del></del>					<del></del> -
4340	11	11	"				<del>  "</del>
4341 4342	11		<u>''</u>		<u>''</u>		<del> !!</del>
4343		<del> </del>	!! !!	11	11		<u>"</u>
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4349	11	,,	11	11	11	<del></del>	<del>                                     </del>
4350	11	11	. 11	11	11	11	<del> ;;</del>
4351	11	11	11	11	11	<u> </u>	<del>                                     </del>
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4356	11	"	11	11	11	. 11	<del>                                     </del>
4357	11	11	11	11	11	11	,,
4358	11	11	11	11	11	11	11
4359	11	11	11	11	11	11	,,,
4360	11	11	11	11	11	11	1 11
4361	11	11	"	11	11	11	"
4362	11	11	11	11	11	11	11
4363	11	11	11	11	**	"	,,
4364	11	11	11	11	11	11	"
4365		11	11	11	11	11	11
4366		11	11	11	11	11	11
4367		11	11	11	11	11	11
4368		11	11	11	11	11	7,
4369	11	11	11	11	11	11	
4370	**	11	"	19	11	11	11
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# Vibration Test

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4322	11 11 11 11 11 11 11
4323         "	11 11 11 11 11 11
4324         "	11 11 11 11 11
4325         """"""""""""""""""""""""""""""""""""	11 11 11 11
4326	11 11 11
4327         II         I	11 11 11 11
4328         """"""""""""""""""""""""""""""""""""	11 11 11
4329         "	11
4330       """"""""""""""""""""""""""""""""""""	11
4331       """"""""""""""""""""""""""""""""""""	
4332       """"""""""""""""""""""""""""""""""""	1 11
4333       """"""""""""""""""""""""""""""""""""	11
4334       ""       <	11
4335       """"""""""""""""""""""""""""""""""""	11
4336       ""       <	11
4337       ""       <	11
4338       "	·····
4339     "	<del></del>
4340     """"""""""""""""""""""""""""""""""""	11
4341     """"""""""""""""""""""""""""""""""""	11
4342     ""	11
4343     """"""""""""""""""""""""""""""""""""	11
4344     II     II     II     II     II       4345     II     II     II     II     II       4346     II     II     II     II     II	11
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4350 " " " " " " "	11
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## Vibration Test

Unit	20-2000-20	Period I	Period II	Period III	Period IV	Period V	Period VI
No.	Cycle Time	3 Hrs	3 Hrs	6 Hrs	12 Hrs	24 Hrs	48 Hrs
4319	10 Min.	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good
4320	11	11	"	11	11	11	11
4321	11	11	l†	11	11	11	11
4322	11	11	11	11	11	11	11
4323	11		11	11	11	<u> </u>	11
4324	11	11	11	11	11	11	11
4325	11	11	11		11	11	11
4326		<u> </u>	11	11	11	11	11
4327		"	11	11	!!	11	11
4328	11	11	11	11	11	11	"
4329		11	11	11	11 11	11	"
4330	11	<del> ;</del>	<del></del>	<u></u>	<del></del>	<del> </del>	<del> </del>
4332		<del> </del>	11		<del></del>	<del></del>	<del></del>
4333	11	<del></del>		<del>,</del> ,		<del></del>	<del>                                     </del>
4334	11	11	11	11		ļ	1
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4336	11	F1	11	11	11		11
4337	11	11	11	11	11	11	11
4338	11	"	11	11	11	"	11
4339	11	91	11	11	11	"	11
4340	11	11	11		11	11	11
4341	11	11	11	11	11	11	11
4342	11	11	11	- 11	11	11	11
4343	11	11	11	11	11	11	11
4344	11	!!	11	11	11	11	11
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4356	11	11	11	11	11	<u> </u>	1, 1,
4357	11	**	11	11	11	11	1,
4358	11	11	11	11	11	11	11
4359	11	11	11	11	11	11	11
4360	11	11	11	11	11	11	99
4361	11	11	11	11	11	11	11
4362		11	11	11	11	11	11
4363		- 11	11	11	11	11	11
4364		11	11		11	11	11
4365	11	11	***	!!	11	11	11
4366	11	11			11	11	11
_4367		11	11	!!	<u> </u>		
4368		11	11		11		11
4369	!!			!!			
4370		11	11	11	11	11	
4371		11	11		!!		!1
1						· · · · · · · · · · · · · · · · · · ·	

III - 1E - 16
Page 8 of 9
Start Date
Completion Date

Vibration Test

Unit	20-2000-20	Period I	Period II	Period III	Period IV	Period V	Period VI
No.	Cycle Time	3 Hrs	3 Hrs	6 Hrs	12 Hrs	24 llrs	48 Hrs
4372	10 Min.	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good
4373	U MIN.	Unit Good	UNITE GOOD	" "	Unit Good	Unit Good	Unit Good
4374	11	11	11	11	11	11	<u> </u>
4375	11	1	11	· · · · · · · · · · · · · · · · · · ·	11	"	<del>  "</del>
4376	11	1 11	11 .	11	11	<u> </u>	<del>"</del>
4377	11	11	11	11	n	11	<u> </u>
4378	11	11	11		11	11	11
4379	11	11	11	11	11	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	,,
4380	11	"	11	"	11	11	1,,
4381	11	· 11	11	11	11	11	1,,
4382	11	11	rt .	11	11	11	11
4383	11	11	11	11	11	11	11
4384	11	11	11	11	11	tt	11
4385	11	11	11	11	11	11	11
4386	**	11	11	11	11	11	11
4387	11	11	11	P1	H	11	11
4388		"	11	11	11	11	"
4389		11	11	11		11	"
4390				H	- 11	11	11
4391		"	11	11		<u> </u>	<u>"</u>
4392		"	!!	"	11	<u> </u>	"
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4394	11	11	11	11	11	11	<u> </u>
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4402	11	11	11	11	,,,	<u>''</u>	11
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4406	*1	"	11	11	11	11	11
4407	*1	11	11	11	11	91	11
4408	t1	11	11	!!	11	11	11
4409	11	11	11	11	11	11	11
4410	11	11	91	11	#1	11	•1
4411	- 11	11	11	11	11	11	11
4412	<b>#</b> 1	11	11	11	11	11	<b>91</b>
4413	11	11	11	11	11	11	11
4414	ti	- 11	11	11	11	11	11
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Page 9 of 9 Start Date Completion Date

Vibration Test

Unit	20-2000-20	Period I	Darfod 17	Ported III	Period IV	Portod V	Period VI
No.	Cycle Time	3 Hrs	3 Hrs	6 Hrs	12 Hrs	24 Hrs	48 IIrs
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4425	10 Min.	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good	Unit Good
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4428	11	11	<del></del>	<del></del>	······	<del></del>	11
4430	11	11		11	<u>''</u>		
4430	11	11	11		11	11	11
4432	11	11	11	<u> </u>	<del> </del>	<del></del>	11
4433	11	11	11	11		<del></del>	11
4434	11	,,	71	11	11	<del></del>	11
4435	11	11	11	11	<del>                                     </del>		11
4436	11	· · · · · · · · · · · · · · · · · · ·		<del></del>		11	11
4437	11	11		11	<del> </del>	11	11
4438	11	<del></del>			<del> </del>	11	
4439	11	11		11	<del></del>	11	11
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4441	11	11	11	f1	11	11	11
4442		11	11	11	<del>                                     </del>	,,	. 11
4443	11	,,	71	11	11	77	11
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4445	11	11		11	<del> </del>	· · · · · · · · · · · · · · · · · · ·	
4446		11		11	11	<del> </del>	
4447	11	••	11	11	11		11
4448	11	11	11	11			11
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4450	11	11	· · · · · · · · · · · · · · · · · · ·	11	<del></del>	11	
4451	11	11	11	. 11	11		11
4452	11	"		11	11	,,	11
4453	11	11	11	11	11	91	<del></del>
4454	11	11	11	11	11	11	
4455	11	· "	11	<del></del>	11	11	11
4456	11	11	. 11	11	11	11	11
4457	11	*1	"		"	11	11
4458	11	11	11	11	11	11	11
4459	11	11	**	11	11	11	11
4460	11	11	. 11	11	11	11	
4461	91	11	11	11	11	- 11	
4462	**	11	11	11	11		
4463	11	11	11	*1	"	77	11
4464	11	11	11	11	11		11
4465	71	11	11	11	11	71	- 11
4466	11	11	H	11		11	.,
4467	11	11	11	11	11		71
4468	11	11	11	11	11	77	- 11
4469		11	11	11	"	11	11
4470	11	11	11	***	11	11	11
4471	11	11	11	11	11	11	11
4472	11	11	(1	11		7	11
4473	11	11	11	11	11	11	- 11
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4475	11						
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4477			n ·	0		,,,	11
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TASK 1E - EXHIBIT 3 STEP-STRESS PROGRAM OUTLINE

SIGNAL COLFS SIDS-FIRESS

	TA 4200 (303 4720)		KEAL/RECOK: TP 1	READ/REGARD TP 2		NEAE/RECOKA	9 TORC	NEAS/NECOKT	66. s.d. 9. RCMS	READ/RECOFD TP 5	72t. E.s. 9 IOURS	READ/RECOKT		ATAD/RECORD		READ RECOKE	SETON SETON	NEAD/RECORT										
	23: 174.1 (309: 47208)		REAL/RECORD	KEAD/NECOND TF 2	3. 0.5 SE 46.9	EAD/NECOLD	64.0 5 305.2	ESAD/RECORD	380 °C = 380 °C = 380°C = 5	200/1940-2 17 5	720 P.	TEAD/RECORD	76C ==: \$ HOLES	TEAD/FEECORD		T t	Baco ast 9 Notites	REAC/RECOM										
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#### SECTION III

# TASK 2 - PREPARATION OF SUBSTRATE FOR EPITAXIAL GROWTH

#### CONTENTS

#### Purpose

## Background

- Step 1 Sawing of Silicon Slices Progress During This Period Conclusions
- Step 2 Mechanical Chemical Polishing of Silicon Slices Progress During This Period Conclusions
- Step 3 Vapor Etching of Silicon Wafers in a Closed Reactor Progress During This Period Conclusions

# Program For Next Interval

- Step 1 Sawing of Silicon Slices
- Step 2 Mechanical Chemical Polishing of Silicon Slices
- Step 3 Vapor Etching of Silicon Wafers in a Closed Reactor
- Exhibit 1 Sketch of Experiment
- Exhibit 2 Sketch of Typical Blade Cut
- Exhibit 3 Sketch of Production Machine

#### SECTION III

#### TASK 2 - PREPARATION OF SUBSTRATE FOR EPITAXIAL GROWTH

J. M. Schroeder

## PURPOSE

In order to obtain the highest degree of integrity in epitaxial material for transistor fabrication, the substrate material and the surface upon which the epitaxial silicon is grown must be of the highest integrity. To accomplish this goal a three step process is proposed whereby:

- 1. The single crystaline silicon ingot is sawed solely by electrochemical methods.
- 2. The slices are polished to a damage free, undistorted mirror surface by mechanical-chemical methods.
- 3. The wafers are etched in the epitaxial reactor prior to deposition to insure removal of foreign material from the wafer surface.

#### BACKGROUND

Prior to the second quarter, experimental work was done in the three areas proposed, namely sawing, polishing and etching in the reactor.

Saw experiments involved the evaluation of a multi-blade wafering machine that cuts with an abrasive and reciprocating blades to minimize crystal damage instead of one high speed diamond wheel.

Wafer polishing techniques were worked out using a mechanical-chemical method. Design and fabrication work was started on an experimental machine that might do this polishing as a production method.

Experiments were designed and made to determine a method of etching polished wafers while enclosed in an epitaxial reactor. Various parameters were explored so that the most advantageous conditions might be selected.

# PROGRESS DURING THIS PERIOD

Step 1 - Sawing of Silicon Slices

The work on crystal sawing involved the evaluation with regard to depth of damage of slices sawed with the Norton multi-blade wafering machine. Rough determinations were made to determine this depth using preferential etches to reveal saw damage at a given depth. The damage to the crystal was found to be less as compared to slices sawed with a high speed diamond wheel, but exact comparisons are difficult because of the variables involved in the methods of damage determination.

Attention was focused on an investigation of the feasibility of electrolitic sawing of silicon whereby the damage to the crystal could be virtually zero.

Simple experiments were performed by biasing a silicon ingot positive and a thin metal blade negative (See exhibit 1). A current was established between blade and crystal through an electrolite of deionized water by rubbing the blade over the crystal. It was found that silicon could be removed by this method as a function of current density and friction. A single slice was cut by this method. The cut made by the blade was straight with virtually no side erosion and removed silicon only where the blade was in contact with the silicon (See exhibit 2). To further evaluate this principle, the multi-blade saw was equipped with brushes and biasing terminals. Two problems were readily apparent:

- With deionized water as an electrolite a very high current density must be maintained requiring a large power supply to accommodate the three hundred blades.
- 2. If electrolitic additives are used to reduce current needed, the additives must be non-corrosive so that the saw will not be attacked. If these problems can be eliminated, silicon slices might be sawed while maintaining the integrity of the grown crystal.

#### **CONCLUSIONS**

The conclusions derived from the saw experiment were as follows:

- All silicon slices sawed with the high speed diamond wheel have damage inflicted into the crystal 40 to 80 microns beneath the surface.
- 2. Slices sawed with six micron abrasive and the multiblade saw are damaged 20 to 60 microns beneath the surface.
- 3. Slices sawed by electrochemical methods are apparently not damaged due to mechanical action.
- 4. Slices might be sawed by electrochemical techniques utilizing the multi-blade saw if an electrolitic solution can be found that will limit current necessary for cutting action and will not corrode the machine.
- 5. Slices cut by this method will retain their dislocation integrity in the slice form.
- 6. Material savings can be realized by cutting thinner wafers and likewise more slices can be obtained per inch of crystal.

Step 2 - Mechanical-Chemical Polishing of Silicon Slices

Over four thousand silicon wafers were mechanically-chemically polished. The process gives extremely planar, mirror like surfaces. Damage to the crystal can be attributed only to air-borne particles falling into the machine and saw chips from the wafers scratching other wafers. A 60% yield can be

realized with the polishing machine as it is in pilot operation. It should be noted that this process begins with sawed slices and takes them directly to the condition "prior to epitaxial deposition". This eliminates rigorous, mounting, lapping, cleaning, etching, and gauging operations that are normally used. The machine currently in use can polish 48 slices per hour (See exhibit 3). One operator could operate two and possibly three of these machines at once. The machine after being used for three months is in operating condition with no major repairs or modifications having been necessitated.

#### CONCLUSIONS

Conclusions to be gathered from the work on the polishing project are as follows:

- 1. The latest model mechanical-chemical polishing machine is operational and capable of producing 60% of 48 slices per hour.
- 2. The process eliminates many of the steps from mounting and lapping to etching and gauging.
- 3. Airborne dust falling into the machine and saw chips on the slices are problems affecting yield.
- 4. To advance the process further a machine incorporating the experience gained in choice of design materials, wafer holding fixtures, acid handling methods, and overall convenience of operation of the machine should be designed and fabricated.
- 5. This machine would be the summation of all experience gained and would render a higher yield, lower cost process with complete emphasis on integrity of material.

Step 3 - Vapor Etching of Silicon Wafers in a Closed Reactor

The process dealing with vapor etching of silicon in a closed reactor, as outlined in the First Quarterly Report, has proven useful. A marked improvement in epitaxial film surface has been noted. Hillocks believed to be nucleated by foreign particles on the surface of the silicon wafer are not present when this method of etching is used, so that virtually no hillocks are formed as the film progresses. The process is in pilot production and is being evaluated for electrical parameters and surface improvement. One additional advantage in this process was found. The epitaxial growth reaction can be balanced by the utilization of HCl additions. From the balanced conditions much can be learned about the rate of reaction and quantities of reaction constituents needed. From this information more process stabilization and regulation can be realized from the growth process itself.

#### CONCLUSIONS

Conclusions derived from experiments dealing with etching of silicon in a closed reactor are as follows:

- 1. The addition of a quantity of HCl gas to the growth constituents drives the reaction in reverse so that non-preferential etching may take place.
- 2. This reaction can take place inside the reactor immediately prior to epitaxial deposition.
- 3. Surface and electrical improvements can be realized from this process.
- 4. Information about the growth reaction can be gained from the utilization of this process.

#### PROGRAM FOR NEXT INTERVAL

Step 1 - Sawing of Silicon Slices

Experiments will be conducted with different electrolites to establish a process for electrolitic cutting of silicon. Modifications will be made to the multi-blade saw to accommodate this process.

Step 2 - Mechanical-Chemical Polishing of Silicon Slices

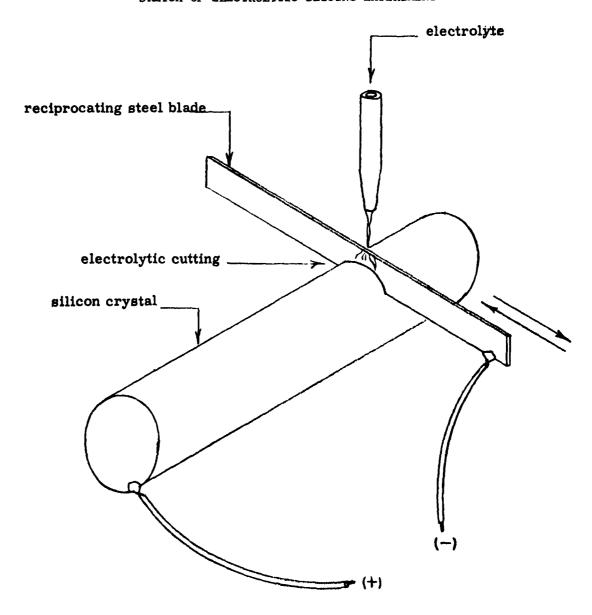
As advanced mechanical-chemical polishing machine will be designed and built to incorporate information gained from prior experiments and operation. The machine will be placed in operation before the end of this quarter for evaluation and modification.

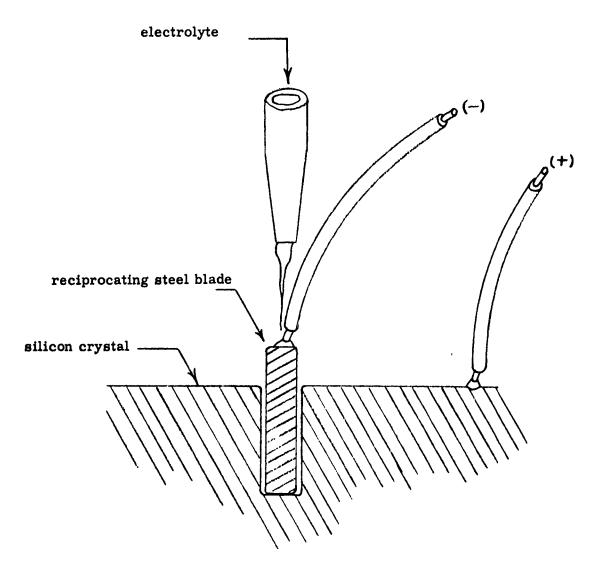
Step 3 - Vapor Etching of Silicon Wafers in a Closed Reactor

Experiments and evaluation of vapor etching in a closed epitaxial reactor will continue until such time as the complete process might be integrated into the form of electrochemically sawed slices from the multi-blade saw, mechanical-chemical polished on the advanced machine and vapor etched in a closed epitaxial reactor.

TASK 2 - EXHIBIT 1

# SKETCH OF ELECTROLYTIC SLICING EXPERIMENT

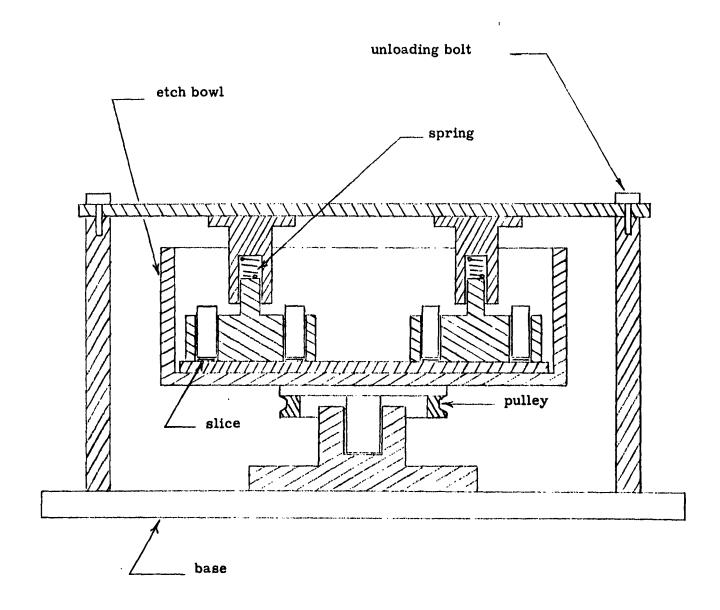




SIDE VIEW SKETCH OF ELECTROLYTIC SLICING BLADE SHOWING CURRENT BLASING AND GEOMETRY OF CUT

TASK 2 - EXHIBIT 2

# SKETCH OF PILOT MECHANICAL - CHEMICAL POLISHING MACHINE



#### SECTION III

#### TASK 3 - IMPROVE HEADER PLATING

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Purpose

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Progress to Date

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Conclusions From Evaluation Number One

Evaluation Number Two

Conclusions From Evaluation Number Two

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Exhibit 2 - Special Test Form #592B

Exhibit 3 - Production Classification Test Card - #592A

Exhibit 4 - Production Classification Test Card - #592B

Exhibit 5 - Summary of Test Results #592A and B

Exhibit 6 - Package Evaluation Log Sheet - Test #SC2A

Exhibit 7 - Package Evaluation Log Sheet - Test #SC2B

Exhibit 8 - Special Test Form #SC2A

Exhibit 9 - Special Test Form #SC2B

Exhibit 10- Production Classification Test Card - #SC2A

Exhibit 11- Production Classification Test Card - #SC2B

Exhibit 12- Tabulation of Results Obtained From Tests #SC2A and B

#### SECTION III

#### TASK 3 - IMPROVE HEADER PLATING

#### P. Rollason

#### **PURPOSE**

Continuing our effort to provide Fairchild Semiconductor with a gold plated header that is equally suitable for NPN and PNP devices, trial and evaluation #1, and trial and evaluation #2 have been completed. The results and documentation on these two runs will be described and conclusions will be drawn from the information obtained.

#### **BACKGROUND**

Prior to the second quarter, a method was developed which showed promise of providing the expected benefits. The necessary gold bath and auxiliary equipment were installed in the plating shop. Plating samples were evaluated and the plating process that showed most promise was used to provide units used in trial and evaluation #1 and #2.

#### PROGRESS TO DATE

## Evaluation #1

The plated units were accepted for production and Special Tests #592A and 592B were originated on 7-17-62. They were completed 8-8-62.

The documentation includes:

- a. Package Evaluation Log Sheets (Submitted with 1st quarterly report)
- b. Special Test Forms #592A and B (Exhibits 1 and 2)
- c. Production Classification Test Cards for Tests #592A and B) (Exhibits 3 & 4)
- d. A summary of the information obtained on these units during Evaluation #1 (Exhibit 5)

Conclusions drawn from Evaluation #1 (Special Test #592A and B)

 The standard plated units checked performed far better than expected on the ISW (150°C) test.

- 2. A discrepancy existed between the production line ISW (150°C) test results and those obtained by read out scope when rechecked in Applications Engineering, Units in following tests will only be checked in ISW (150°C) on a scope in Applications.
- 3. Since the expected difference in ISW (150°C) reject level on the two types of plating did not occur due to the unforseen improvement in the standard plate, further evidence in the form of Evaluation #2 is awaited before further conclusions can be drawn.

#### Evaluation #2

The plated units to be used for Evaluation #2 were accepted for production and Special tests #SC2A and SC2B were initiated on 9-17-62 and completed 10-22-62.

Documentation for Evaluation #2 includes:

- a. Package Evaluation Log Sheets (Exhibits 6 and 7)
- b. Special Test Forms #SC-2-A, and #SC-2-B (Exhibits 8 and 9)
- c. Production Classification Test Cards for Tests SC-2-A and B (Exhibits 10 and 11)
- d. Summary of information obtained during evaluation #2 (Exhibit 12)
  Conclusions drawn from Evaluation #2.
  - 1. The unexpected improvement in the Standard Plating continued as far as ISW (150°C) is concerned.
  - 2. The improvement in the Standard Plate must be investigated since if it continues during Evaluation #3 and #4 it renders invalid the present system of detecting improvements in plating. If the Standard Plate can be controlled, it promises plating of high quality which can be applied more economically than the special or duplex plate.
  - 3. Since the expected benefits of the special plate rely on the removal of the effect of the Kovar structure on the plate, it is possible that the performance of the standard gold can be related to a factor such as headers composed of fine structured Kovar. All

DP No. 1 Appx. 1

## FAIRCHILD SEMICONDUCTOR CORPORATION

Special Test

• • • • • • • • • • • • • • • • • • •	No. <u>59</u> 2 A
Originator J. S. Horton Rolling	Date 7-17-62
	16-12pproved Product Manager) Date 197-16
FT-TYPE TA-4500	Unit Identification
Starting Date	(Rum No., etc) ) Originator
Die Sort Target Date	(Initials)
Classification Target Date	) Prod. Mgr. (Initials)
Contact originator as indicated on at material at any point not checked.	tached Routing Check-off List. Do not hold
PURPOSE OF THIS TEST: To evaluate at  As control for special	andard Fairchild plating on Signal Corp. Project.
INSTRUCTIONS: (Be specific - Attach Die attach 150 TO-5 headers (S	separate Flow Chart, if necessary)
Use 4500 dice, normal Pro	cess through classification.
2. Maintain header batch ide	entity.
3. Save all rejects as indic	ated below.
Die Attach	
b. Lead Weld	
c. Classification	•
Use cost no 11-9010-	ch separate report 1 paces dary
CONCLUSIONS & RECOMMENDATIONS. (Atte	ch constate report (if necessary)

 $\mathcal{B}$ 

# FAIRCHILD SEMICONDUCTOR CORPORATION

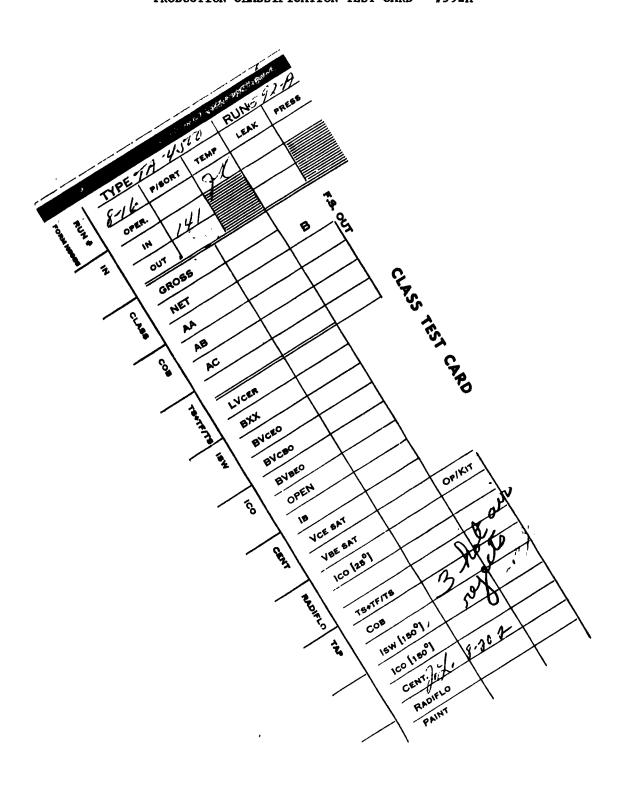
Special Test

$A_{2}$
Originator J. S. Horton Kellan Date 7-37-52
Approved   Date 7-16-6 Approved   Date   Tul 62   (Originator's Dept) (Product Manager)
FT-TYPE TA-4500 Unit Identification (Rum No., etc)
Starting Date Originator (Initials)
Die Sort Target Date)
Classification Target Date ) Prod. Mgr. (Initials)
Contact originator as indicated on attached Routing Check-off List. Do not hold material at any point not checked.
PURPOSE OF THIS TEST: To evaluate special gold planing on Signal Corp. Project.
$(S_{n-1}, L B)$
INSTRUCTIONS: (Be specific - Attach separate Flow Chart, if necessary)  Die attach 150 special plated TO-5 headers.
<u> </u>
Use 4500 dice, normal process through classification.
(2) Maintain header batch identity.
Save all rejects as indicated below.
(a.) Die Attach
b. Lead Weld
c. Classification
there count 76 - 11-9010-0000 per off go, the Think there is
CONCLUSIONS & RECOMMENDATIONS: (Attach separate report, if necessary)
The family of Standard (Samuel 18412) to some
from some lot t

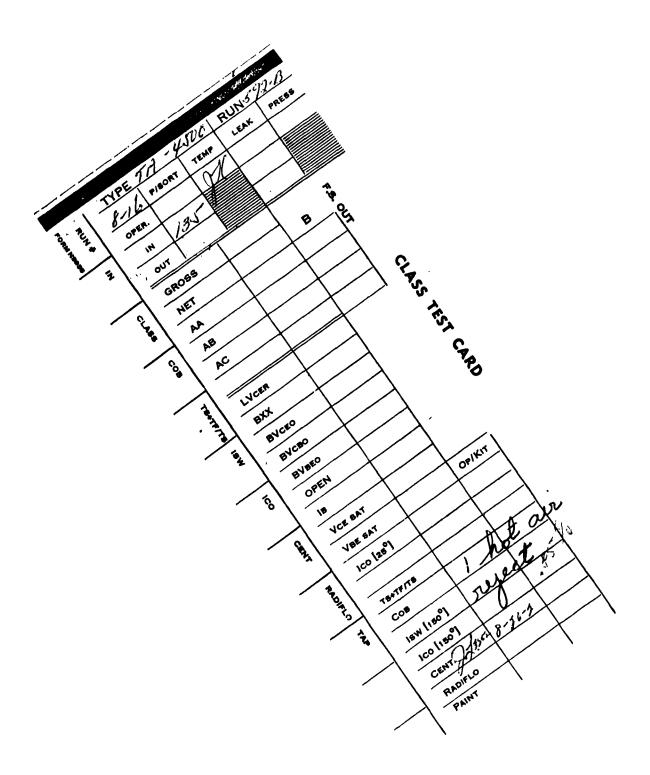
III - 3 - 6

TASK 3 - EXHIBIT 3

PRODUCTION CLASSIFICATION TEST CARD - #592A



# PRODUCTION CLASSIFICATION TEST CARD - #592B



TO: H. Roos

FROM: J. S. Horton

SUBJECT: Signal Corp. Plating Project.

CC. N. Peterson P. Rollason

First batch of TO-5 headers plated for Signal Corp. Project were evaluated by Packaging Group 7-16-62 and Special tests written and approved 7-19-62. Special tests were delayed until 8-2-62. Units were processed through final seal by Process Development line and classified by Production. Results were as shown below.

1st Run Signal Corp. Plating Project

	Standard	Duplex
	Plating	Plating
Run Number	592-A	592-B
No. Units Per Run	150	150
Туре	P.N.P. dice - S	plit lot of 300
Die Attach	Good	Good
Lead Bond	Good	Good
Lead Weld	Good	Good
% Yield at Classification	89.5	86.7
Vce Sat (Fall out)	0	0
Isw (150°) Prod. Equip. (Fall out)	2.4%	0.85%
Isw (150°) Read out on Scope (Fall out)	2.4%	5.99%
Solderability	Fair to Good	Fair

Test units shall be returned to P. Rollason for retaining until completion of Project.

TO:

H. Roos

FROM:

J. S. Horton

SUBJECT: Signal Corp. Plating Project.

CC. N. Peterson

P. Rollason

First batch of TO-5 headers plated for Signal Corp. Project were evaluated by Packaging Group 7-16-62 and Special tests written and approved 7-19-62. Special tests were delayed until 8-2-62. Units were processed through final seal by Process Development line and classified by Production. Results were as shown below.

1st Run Signal Corp. Plating Project

Standard Plating	Duplex Plating
592-A	592-B
150	150
P.N.P. dice - S	Split lot of 300
Good	Good
Good	Good
Good	Good
89.5	86.7
0	0
2.4%	0.85%
2.4%	5.99%
Fair to Good	Fair
	Plating  592-A  150  P.N.P. dice - S  Good  Good  Good  89.5  0  2.4%  2.4%

Test units shall be returned to P. Rollason for retaining until completion of Project.

# TASK 3 - EXHIBIT 6 FAIRCHILD SEMICONDUCTOR CORPORATION

# PACKAGE EVALUATION LOG SHEET

Re	ec. F	Report	#	
QA	Rec.	Rej.	Rер.	#

# Process Development

SIGNAL CORP

Date	8-28-62

OperatorEvans	
Type of Test Preliminary Evaluation	· · · · · · · · · · · · · · · · · · ·
Purpose To evaluate Au Plating and check hermetic seal.	
Vendor Fsc Rec. Date 8-28-62 Type Component TO-5	Lot
Drawing No FSC-40176 No. Units 3,000	Material Kovar
Package Description TO-5 HDR. Standard Plating - Test #2	
PROCEDURE	MACHINE SETTINGS
1. Die Attach 10 Units with Reject Dice.	
2. Age 10 Units for 60 Hrs. in N <sub>2</sub> at 300°C.	
3. Temp Shock 20 Units at 390°C - 400°C for 20 Sec.	
4. Final seal 20 Units in the He and VEECO Check.	
5. Check Physical Dimensions.	
6. Etch Test for 40 Sec. in CP-8.	
7. Check Lead Solderability.	
8. Insulation Resistance at 10 <sup>-10</sup> Test Voltage of 250 V. DC.	
SULTS OR CONCLUSIONS	]
1. Die Attached 10 units - Preform Wet Out Good.	
2. Aged 10 Units - No Discoloration - Good Dice Adhesion.	į
3. Temp Shocked 20 Units - No Discoloration.	
4. Sealed 20 Units - No Leakers at VEECO.	
5. Physical Dimensions - Within Spec.	
6. Etch Test - Good.	
7. Lead Solderability - Fair.	1
8. Insulation Resistance - Within Spec.	
MISC. INFORMATION	1
1. These Units for test #SC2A.	
DISPOSITION OF COMPONENTS	
1. Units OK for Production.	[
	1

# FAIRCHILD SEMICONDUCTOR CORPORATION

# PACKAGE EVALUATION LOG SHEET

Rec. Report #\_\_\_\_\_\_QA Rec. Rej. Rep.#\_\_\_\_\_

# Process Development

SIGNAL CORP

Date 8-28-62

Operator Evans									
Type of Test Preliminary Evaluation									
Purpose To Evaluate Au Plating and Check Hermetic Seal.									
Vendor Fsc Rec. Date 8-28-62 Type Component TO-5	Lot								
Drawing No FSC-40176 No. Units 3,000 Material	Kovar								
Package Description TO-5 Standard HDR Duplex Plating - Test #2									
PROCEDURE	MACHINE SETTINGS								
1. Die Attach 10 Units with Reject Dice.									
2. Age 10 Units for 60 Hrs. in N <sub>2</sub> at 300°C.									
3. Temp Shock 20 Units at 390°C - 400°C for 20 Sec.									
4. Final Seal 20 Units in He and VEECO Check.									
5. Check Physical Dimensions.									
6. Etch Test for 40 Sec. in CP-8.									
Check Lead Solderability.									
Insulation Resistance at $10^{-10}$ Test Voltage of 250 V. DC.									
RESULTS OR CONCLUSIONS									
1. Die Attached 10 Units - Preforms Wet Out Good.									
2. Aged 10 Units - Some Discoloration - Good Dice Adhesion.									
3. Temp Shocked 20 Units - No Discoloration.									
4. Sealed 20 Units - No Leakers at VEECO.									
5. Physical Dimensions - Within Spec.									
6. Etch Test - Good.									
7. Lead Solderability - Fair.									
8. Insulation Resistance - Within Spec.	}								
MISC. INFORMATION									
1. These Units for SC2B.									
DISPOSITION OF COMPONENTS									
1. Units OK For Production.									
	1								
	1								

See other side for additional comments.

## FAIRCHILD SEMICONDUCTOR CORPORATION

Special Test No. SC-2-A

Originator	Peter Rollason	Date	9/17/62
Approved ///	ginator's Dept)	Z Approved (Prod	Date luct Manager)
FT-TYPE_	TA 4500	_Unit Identificat	
	et Date	) _ ) Originator )	(Initials)
	n Target Date	) Prod. Mgr.	(Initials)
	nator as indicated on attac ny point <u>not</u> checked.	hed Routing Check	c-off List. Do not hold
PURPOSE OF TH	IS TEST: To evaluate stand	ard Fairchild pla	ting for Signal Corps contract
INSTRUCTIONS:	(Be specific - Attach sep	arate Flow Chart,	if necessary)
	Die attach 150 standard pl 1. Use 4500 dice (see belo 2. Maintain header batch 3. Save all rejects as indi a. die attach b. lead weld. c. classification 4. Use charge # 11-90-10	ow) normal proce identity. lcated below.	ss through classification.
ONOT HELONG 6	nncologum Amtolica (Assault		
JUNCLUSIONS &	RECOMMENDATIONS: (Attach  Dice used on the standard		ing lots (samples A and B)

must originate from the same dice lot.

## FAIRCHILD SEMICONDUCTOR CORPORATION

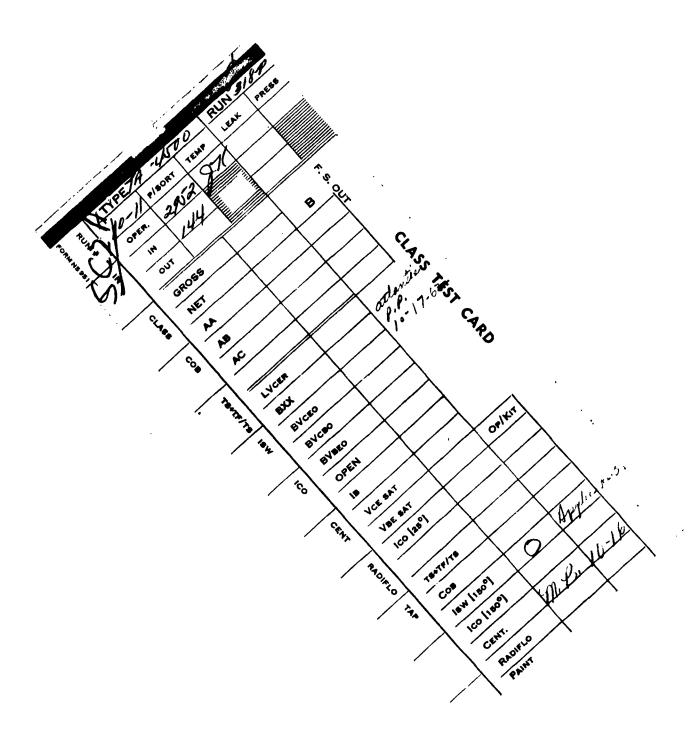
Special Test No. SC-2-B

Originator Peter Rollason	Date
Approved Its Relation Date 9/1	/7/6~ Approved Date (Product Manager)
FT-TYPE TA 4500	Unit Identification
Starting Date	(Rum No., etc) ) Originator (Initials)
Die Sort Target Date	· · · · · · · · · · · · · · · · · · ·
Classification Target Date	) Prod. Mgr. (Initials)
Contact originator as indicated on a material at any point not checked.	ttached Routing Check-off List. Do not hold
PURPOSE OF THIS TEST:	
To evaluate special plati	ing for Signal Corps contract.
INSTRUCTIONS: (Be specific - Attach	separate Flow Chart, if necessary)
Die attach 150 special pl 1. Use 4500 dice (see be 2. Maintain header batch 3. Save all rejects as in a. die attach b. lead weld c. classification	lated headers (Sample B) elow) normal process through classification. h identity
ONCLUSIONS & RECOMMENDATIONS: (Atta	ach separate report, if necessary)

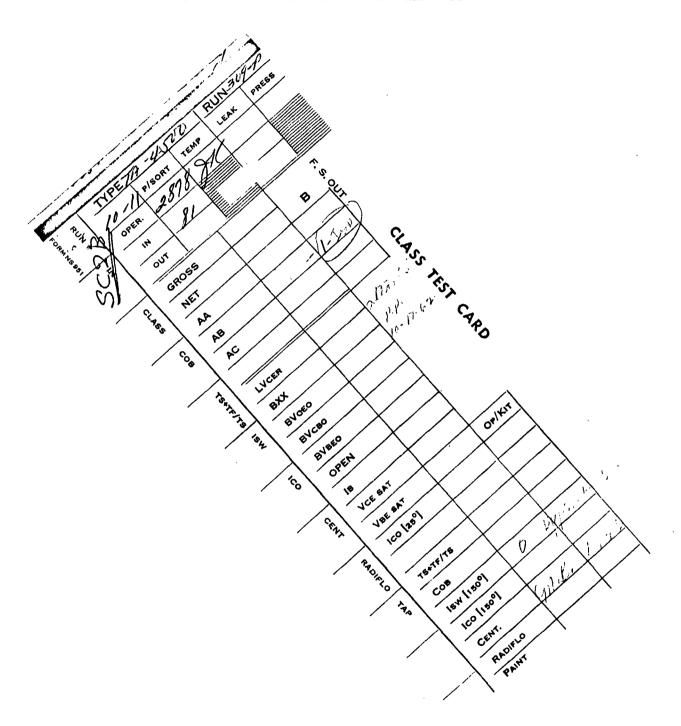
Dice used on standard and special plating lots (samples A and B) must originate from the same dice lot.

TASK 3 - EXHIBIT 10

# PRODUCTION CLASSIFICATION TEST CARD - #SC2A



# PRODUCTION CLASSIFICATION TEST CARD - #SC2B



TO:

H. Roos

FROM:

J. S. Horton

SUBJECT: Tabulation of Results Obtained From Special Test #SC2A and SC2B.

Units evaluated by Packaging Group 8-28-62.

Special test originated 9-17-62.

Special test completed 10-22-62.

2nd Ru	n Signal	Corp.	Plating	Project
--------	----------	-------	---------	---------

	Standard Plating	Duplex Plating
Special Test Number	SC2A	SC2B
Run Number	318P	309P
Dice type	P.N.P. (Split lot)	
Die attach	Good	Good
Lead Weld	Good	Good
Lead Bond	Good	Good
% Yield at classification	87.6	91.3
Vce Sat (Fall out)	1	1
Isw (150°C) Read out on Applications Scope	O	0
Solderability	Fair	Fair

#### SECTION III

# TASK 4 - ISOLATE AND ELIMINATE SOURCES OF GROSS PARTICLES CONTENTS

## Purpose

Progress and Future Plans

Prevent Creation of Particles

Weld Schedule

Electrode Finish

Keep Existing Particles Away From Unwelded Transistors

Lead Guide Cones

New Plexiglas Dryboxes

Pre-Assembly of Cans

Particle Detection

Exhibit 1 - Photograph - Existing Steel Drybox

Exhibit 2 - Photograph - Existing Steel Drybox

Exhibit 3 - Photograph - New Plexiglas Drybox

Exhibit 4 - Photograph - New Plexiglas Drybox

Exhibit 5 - Photograph - New Plexiglas Drybox

#### SECTION III

#### TASK 4 - ISOLATE AND ELIMINATE SOURCES OF GROSS PARTICLES

P. M. Weiler

#### **PURPOSE**

The purpose of this task is to prevent particles which are detrimental to the operation of a transistor from getting into otherwise good transistors, and to detect particles which do get in transistors. The critical process step in keeping particles out of transistors is the final seal operation. We are attempting to eliminate particles in finished units at this station by: 1. Preventing creation of particles, 2. Keeping existing particles away from uncapped transistors, and 3. Detecting particles in finished units.

#### **PROGRESS**

Prevent Creation of Particles.

Weld Schedule - Work finished and report written in last report.

Electrode Finish - During the last quarter, we tried electrodes with ground finishes and found no change in splash. We tried lowering the welder pressure to induce splash and compared the occurence of splash with the ground electrodes with splash occurence of unground electrodes and it was found that there was no significant change in splash, in fact, splash appeared to occur slightly more often with the ground electrodes.

Keep Existing Particles Away From Unwelded Transistors

Lead Guide Cones - The purpose of Lead Guide Cones is to form a shield around the final seal electrode during the actual welding operation. Tool Design has finished a new design of the lead guide cones. However, this new design requires a change in electrode changes forthcoming, it has been decided to postpone testing the new cones several months, until other electrode changes are fixed.

New Plexiglas Dryboxes - The purposes of the new plexiglas drybox are:

1. Prevent any particles outside the drybox from entering into the unfinished transistor area, 2. Provide shielding inside the drybox for uncapped transistors, 3. Provide better visibility of the inside of the drybox and thus give a better indication of cleanliness, 4. Provide an easily cleaned drybox, and 5. Eliminate as far as possible any metal parts in the drybox which might produce metallic particles.

The new plexiglas drybox has been completely designed and has proven successful in the area of cleanliness. The box proves adequate in being air-tight and keeping the uncapped units shielded from any weld splash. The boxes provide a maximum visibility of the inside and lend themselves to cleanliness because of the ability to easily observe the state of cleanliness inside. They have also proved easy to clean because of the good visibility and absence of many 'hooks and crannies" where particles could collect. The new boxes also are constructed of almost all non-metallic materials and thus provide very little chance of generating objectionable particles. Exhibits 1 and 2 show the dryboxes which we have been using. They were made from stainless steel with visibility only from the top. The floors were covered with a black tape to prevent the generation of metallic particles from materials sliding over the steel floor, but the tape in turn created "nooks and crannies" for particles to collect. The best tape available was black plastic tape, but this plus the limited visibility restricted the observation of cleanliness. Exhibits 3, 4, and 5 show our new plexiglas drybox, and especially shows the improved visibility of the box. The bottom of the box is made of Phenolic, which is easily cleaned, has a hard surface to prevent particle generation and is a non conductor so that if any particle that might be generated should get into a finished transistor, it would not electrically affect the transistor. All other parts, as far as possible, are made from plastic or nylon to prevent the generation of metallic particles. The box is easily cleaned (most accessory parts shown are easily removed for cleaning) and the cleanliness (or dirtiness) is easily observed.

Thus, we feel that we have a superior drybox which provides excellent visibility, is easily cleaned, and prevents creations of particles.

To date, we have installed a plexiglas drybox on the 2N914 line. The 2N995 line will have the plexiglas drybox installed by the end of February.

Cleaning Procedure - New procedure has been in force on plexiglas dryboxes since about June. The procedure is proving adequate and will be included on new dryboxes as they are installed.

Pre-assembly of Cans - This project appears very promising and we now feel

that this project will prove the best means of keeping particles away from the transistors. Our hope is to develop a jig which will place the cans onto the headers prior to the welding operation.

The procedure will be:

- Remove transistors in a pre-assembly rack, and cans in a pre-assembly rack from the vacuum bake over into the right hand side of the drybox. (There will be a partition in the middle of the drybox to protect the uncovered transistors from any splash.)
- Place can rack in the jig in which the cans are inserted and held (open end down) by a vacuum chuck.
- 3. Place the rack of transistors under the cans which are suspended by the vacuum chuck.
- 4. Lower the cans onto the transistors. Because the cans and headers are in racks that hold each item in accurate relationship with all other items and with the unit they fit with, this can be done without much trouble. The means of lowering the vacuum chuck with the cans is by an air cylinder. After the cans are assembled on the headers, full pressure is applied to the cylinder and the cans are crimped to the header. However, it is not yet certain that this function can be performed and it will have to be tested. If it cannot be worked, the cans will still be pre-assembled, but the crimping action will not be used.
- 5. The transistors with the cans pre-assembled on them are passed into the welder section of the drybox for final sealing and will be protected from any splash.

Pre-assembly appears to be an excellent means of preventing gross particles from being sealed in a transistor, because the open cans and uncovered headers are not allowed to be near the welder where they could pick up stray particles. Rather, the headers are capped immediately after entering the drybox, thus keeping down the time which they are uncovered, and the transistors are covered at all times when they are around the welder where there is the danger of weld splash.

To date, we have completed the preliminary design of the jigs and obtained quotes on the equipment necessary. We are now waiting

for fabrication of a prototype jig with which we will test the principle. If it works well we will fix the design and have pre-assembly jigs made for each final seal welder.

### Particle Detection

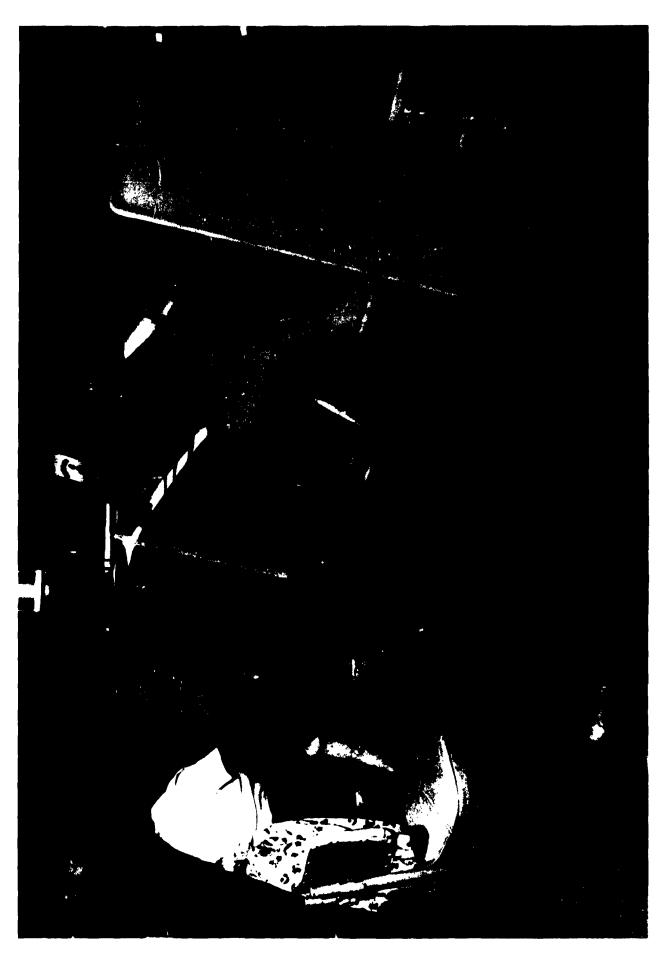
In an effort to check for the presence of particles in finished transistors, we have been testing a high frequency amplifier. The test will be to shake a transistor in question near the amplifier's microphone. If there is a gross particle inside, it will "rattle" with a high frequency sound, and the amplifier will pick up the sound and amplify it for an operator to hear.

In test of the equipment it has been found to work well with large particles such as loose dice, preforms, and chunks of metal down to about 1/64" diameter. These particles produce a distinct sound in the amplifier. Particles smaller than this are questionable at this time and require more testing.

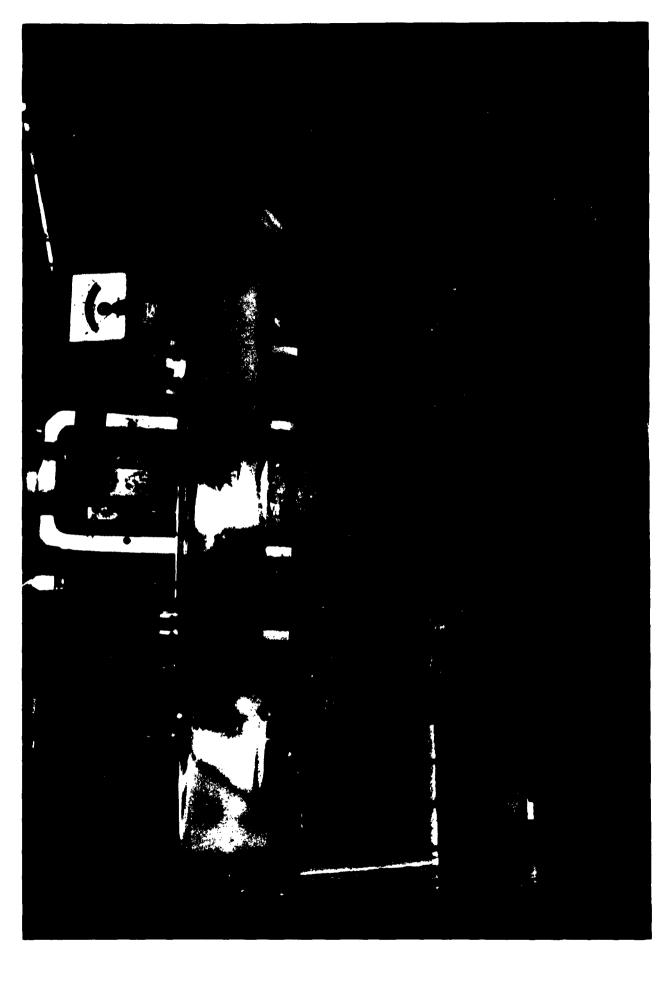
A jig was fabricated for testing transistors with the amplifier, but the jig produced so much background noise that it was undesirable and is back for redesign. There are also new ideas for the jig which will be tried as they are fabricated. The amplifier appears to work well for large particles and was able to clearly pick out at least one transistor with a particle in a 250 unit batch. We believe it will work well and are now waiting for the new jigs in order to test in large volumes.











### SECTION III

### TASK 5 - PREFORM ELIMINATION

### CONTENTS

### Abstract

Narrative and Data

Conclusions

Program for Next Interval

Exhibit 1 - Equipment

Exhibit 2 - Electrical Test Conditions

Exhibit 3 - Frequency Distributions ( 9 pages)

### SECTION III

### TASK 5 - PREFORM ELIMINATION

G. E. Davis

### ABSTRACT

Vacuum evaporation of preform material onto the backsides of 2N914 and 2N995 devices is proving to be an effective assembly method.

NARRATIVE AND DATA

During the time period covered by the previous quarterly report, doped gold preform material was vacuum evaporated onto the backsides of 2N914 and 2N995 devices. In the case of the NPN device, 2N914, the preform material contained 99.7 percent gold and 0.3 percent antimony. The p-type material used on 2N995 devices was 99.3 percent gold and 0.7 percent boron. A description of the procedure and equipment used to perform the evaporation may be found in the Task 5 section of the previous quarterly report.

At the time of the writing of the previous report the assembly of 2N995 devices was incomplete. Assembly and classification of these units are now complete. Classification data show that 73.5 percent of the units classified were good units. There were no  $V_{ce}$  (sat.) or  $V_{be}$ (sat.) failures.

Further data on 2N995 devices are available, but haven't been analyzed as yet; therefore  $V_{ce}$  (sat.) is the most important parameter to consider in a preform elimination test, since it is an indicator of the contact resistance between the die and the header.  $V_{be}$  (sat.) is also considered because it is an indicator of the lead bond quality. Lead bond quality could be affected by the elimination of the separate preform in the assembly of devices. For these reasons, only these two parameters and the switching current,  $I_{sw}$ , will be considered in the evaluation of the devices assembled without separate preforms. No  $I_{sw}$  data is available at this time.

The 2N914 devices assembled during the period of the previous quarterly report are now being placed in the test program described in that report.

Zero hour data are available and frequency distribution charts of this data may be found in Exhibit 3. Three separate frequency distributions for each parameter were constructed, one for each of the test phases.

Superimposed on each distribution is the distribution of a control group. The units in the control groups were drawn from finished inventory; these units were assembled using the normal preform assembly procedure.

Inspection of the distributions of  $V_{be}$  (bat.) and low current  $V_{ce}$  (sat.) will show that there is no significant difference between the test groups and the control groups. Similar inspection of the distributions of high current  $V_{ce}$  (sat.), however, will show that the test group is significantly better than the control group. In general, high current  $V_{ce}$  (sat.) is lower and the distribution tighter for the test units than for the control units.

### CONCLUSIONS

Data obtained to date have shown that vacuum evaporation of preform material is an effective method of eliminating the necessity for the use of separate preforms in the assembly of 2N914 and 2N995 devices. In the case of high current V<sub>ce</sub> (sat.), the units assembled without separate preforms are superior to those assembled in the normal manner.

### PROGRAM FOR NEXT INTERVAL

During the next interval all units, both test and control, shall be subjected to the testing program described in the previous report. Data should be recorded and analysed after 250 hours, 500 hours and 1000 hours of testing. Zero hour data for 2N995 devices shall also be analysed. Any units which fail during the test shall be withdrawn from the test and the failure mode identified and investigated.

### TASK 5 - EXHIBIT 1

### **EQUIPMENT**

- 1. Classification Equipment (2N995): Type 4 Transistor Tester, fabricated by Instrumentation Department, Fairchild Semiconductor.
- 2. Life Test Parameter Test Equipment: Fairchild Datalogger, fabricated by Instrumentation Department, Fairchild Semiconductor.

### TASK 5 - EXHIBIT 2

### ELECTRICAL TEST CONDITIONS

1. 2N914



# FREQUENCY DISTRIBUTION

VBE (SAT.)

SIGNAL CORPS

Ic = 10me, IB = 1ma PREFORM ELIMINATION

ACCEPTANT CARRIES ACCEPTANT PREFERENCE TREST CONTROL STREAMENT SETTEMENT SE 200/21/2002 DYNAMIC TEST DEVICE TO 1341 (24914)

VBE (SAT), VOLTS

PERCENT

College

1337

NO. 31,172. POLYPURPOSE:



# FREQUENCY DISTRIBUTION

VBE (SAT.)

DEVICE 73 /34/ (34 9/4)

SIGNAL CORPS

PREFORM ELIMINATION

I<sub>B</sub> = /mg 10 mes

ENTROL GROUP, MPRHAL PREPOEN

ESSENISS TEST GROUE: EVAPODENED PREES Dal 3 IVS SI SUM WS OPERATING LIEE TEST 12.21

VBE (SAT), VOLTS

TASK 5 - EXHIBIT 3

III -5 - 6

# FREQUENCY DISTRIBUTION

VBE (SAT.)

SIGNAL CORPS DEVICE TE 1341 (2MFM)

PREFORM ELIMINATION

Ic = 10ma, IB = 1ma

TEST GOME EVERYFUED METERS CONTROL GROUP: NOR MALL PREFACE STAPLE SIZE SO

VBE (SAT), VOLTS

"/11/kz 660

BE

TASK 5 - EXHIBIT 3

7 - 12 - 22 - 22

V<sub>CE</sub> (SAT.)

DEVICE 715 1341 (2NPIE) SIGNAL CORPS

SEFFICE TOO

PREFORM ELIMINATION

DYNAMIC THEY THE MENDING TO THE PARTER = 10 mg, IB = 1 mg

FREQUENCY DISTRIBUTION

### FREQUENCY DISTRIBUTION

V<sub>CE</sub> (SAI.)

SIGNAL CORPS

PREFORM ELIMINATION

Ic = 10 ma , IB = 1 ma

O SEGUE REGULATION DEVICE 7/3 / 34/ (249/4)

V<sub>CE</sub> (SAT), VOLTS

TASK 5 - EXHIBIT 3

PERCENT III - 5 - 9 NORWOOD, MASSACHUSETTS.

### FREQUENCY DISTRIBUTION

V<sub>CE</sub> (SAT.)

DEVICE TIS 1341 (2NSIY)

SIGNAL CORPS

PREFORM ELIMINATION

I<sub>C</sub> = 10ma, I<sub>B</sub> = 1ma

SANDLE SILE YOUR SILE YOUR SILE YOUR SALESFUL TO SEE SHALL TO SEE SHALL TO SEE SHALL TO SEE SHALL STATE STAT SAMPLE SIZE SC

V<sub>CE</sub> (SAT), VOLTS

TASK 5 - EXHIBIT 3

V<sub>CE</sub> (SAI), VOLIS

V<sub>CE</sub> (SAT.)

NO. 31,172. FOLYPURPOSE: 44 SY 100 DIVISIONS.

SIGNAL CORPS DEVICE<u>TB 1341 (24 914)</u>

FREQUENCY DISTRIBUTION

PREFORM ELIMINATION

Ic = 200 ms. IB = 20 mg.

CONTROL GROUP: NOPMEL PREFOR

SAMPLE SIZE Z

SAMPLE SIXE IN

WWARIC TEST

O HEUR REAUDUS

PERCENT III - 5 - 11

NO. 31,172. POLYPURPORE:

CODEX BOOK COMPANY, INC.

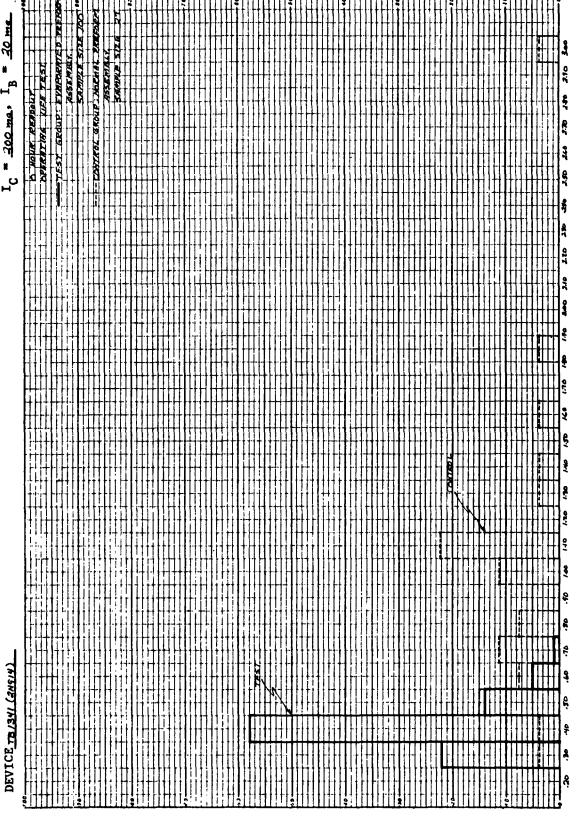
### FREQUENCY DISTRIBUTION

V<sub>CE</sub> (SAT.)

SIGNAL CORPS



PREFORM ELIMINATION



NORWOOD, MASSACHUSETTS.



### FREQUENCY DISTRIBUTION

VCE (SAL.)

SIGNAL CORPS

PREFORM ELIMINATION

I<sub>C</sub> = 200mg, I<sub>B</sub> = 20mg

TEST GROVE ' h I BROWNED SKETS 24.97 E 517F 47. SOUT STREAM TEST O MAKE WEADOOT DEVICE 78 /34/ (2/15/4)

TASK 5 - EXHIBIT 3 V<sub>CE</sub> (SAI), VOLIS

PERCENT

### SECTION IV

### CONFERENCES

On October 18, 1962 a conference was held in Mountain View, California between representatives of the Army Electronic Materiel Agency and Fairchild Semiconductor personnel. At the meeting the objectives, progress, and plans for this contract were reviewed. Equipment and processes being improved under terms of this contract were examined by the U.S.A.E.M.A. representatives.

### SECTION V

### IDENTIFICATION OF TECHNICIANS

In August, September and October of 1962, 4608.5 hours were expended by Fairchild engineering and support personnel on this contract, exclusive of technical supervision whose time is accumulated in overhead accounts.

Following is a list of key technical personnel and time expended by them.

### SUMMARY OF HOURS CHARGED BY KEY TECHNICAL PERSONNEL

	Hours
Armold C	12.0
Arnold, S.	13.0
Butts, W.	177.5
Carmichael, E.	60.0
Corzine, J.	221.5
Davis, G.	33.0
Fitch, W.	8.0
Gregory, A.	76.0
Hammer, R.	258.5
Horton, J.	10.0
Kobrin, D.	186.0
Levitsky, M	33.0
Martin, D.	126.0
Murphy, J.	292.0
Myers, D.	32.0
Rollason, P.	61.0
Schroeder, J.	204.0
Sentous, J.	104.0
Shea, D.	52.0
Simionato, J.	16.0
Tsang, W.	1.0
Vaatveit, E.	12.0
Warren, W.	397.0
Weiler, P.	100.0
, ··	100.0